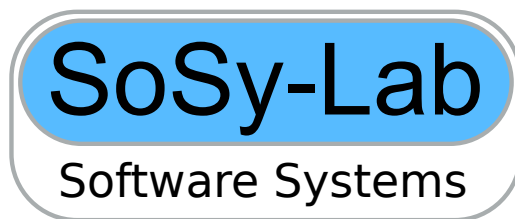


# Sliced Path Prefixes: An Effective Method to Enable Refinement Selection

Dirk Beyer, Stefan Löwe, Philipp Wendler



**We want Refinement Selection !!!**  
**Because straight-forward interpolation**  
**completely and utterly sucks !!!11!**

Dirk Beyer,

Stefan Löwe

Philipp Wendler



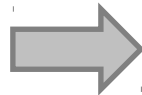
# Software Verification

Goal: Build an **automatic** software verifier

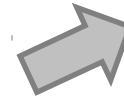
C program

```
int main() {  
  int x = 10;  
  int y = 3;  
  int z = x+y;  
  assert(z > 0);  
}
```

specification



software  
verifier



**SAFE**

i.e., assertions  
cannot be violated



**UNSAFE**

i.e., there is a bug  
in the program

# Software Verification

Goal: Build an **automatic** software verifier

C program – Linux Device Driver

```
int lock_mutex_unlock_usb_hsa_list_lock(struct mutex *lock)
{
    int nondet_res;
    {
        #line 1495
        if (!lock_mutex_unlock_list_lock == 1) {
            #line 1498
            nondet_res = __VERIFIER_nondet_int();
            #line 1501
            if (nondet_res == 1)
                return (0);
            #line 1504
            else {
                #line 1505
                return (1);
            }
        }
        #line 1511
        return (1);
    }
}
#line 1520 "/work/vladimir/OPDS/Work/Kernel/current-3.10/drivers/usb/core/usbcore.h:8: default:3.2.3-0-32_7a-0-ufa/line:3.7.3: kind: bug: 201809: symbol: futex_wait_timeout(2, 0, common: mode/15a_common_mutex.c"
void lock_mutex_unlock_usb_hsa_list_lock(struct mutex *lock)
{
    #line 1523
    if (!lock_mutex_unlock_list_lock == 2) {
        #line 1525
        } else {
            #line 1527
            lock_error();
        }
        #line 1531
        lock_mutex_unlock_list_lock = 2;
        #line 1534
        return;
    }
    #line 1538 "/work/vladimir/OPDS/Work/Kernel/current-3.10/drivers/usb/core/usbcore.h:8: default:3.2.3-0-32_7a-0-ufa/line:3.7.3: kind: bug: 201809: symbol: futex_wait_timeout(2, 0, common: mode/15a_common_mutex.c"
    static int lock_mutex_unlock_list_lock;
    #line 1541 "/work/vladimir/OPDS/Work/Kernel/current-3.10/drivers/usb/core/usbcore.h:8: default:3.2.3-0-32_7a-0-ufa/line:3.7.3: kind: bug: 201809: symbol: futex_wait_timeout(2, 0, common: mode/15a_common_mutex.c"
    int lock_mutex_unlock_list_lock;
    {
        int nondet_res;
        #line 1538
        if (!lock_mutex_unlock_list_lock == 1) {
            #line 1541
            lock_error();
            #line 1543
            nondet_res = __VERIFIER_nondet_int();
            #line 1545
            if (nondet_res == 1)
                return (0);
            #line 1547
            lock_mutex_unlock_list_lock = 2;
            #line 1549
            return (0);
        }
        #line 1552
        return (-4);
    }
}
#line 1557 "/work/vladimir/OPDS/Work/Kernel/current-3.10/drivers/usb/core/usbcore.h:8: default:3.2.3-0-32_7a-0-ufa/line:3.7.3: kind: bug: 201809: symbol: futex_wait_timeout(2, 0, common: mode/15a_common_mutex.c"
int lock_mutex_unlock_list_lock(struct mutex *lock)
{
    int nondet_res;
    #line 1562
    if (!lock_mutex_unlock_list_lock == 1) {
        #line 1565
        } else {
            #line 1567
            lock_error();
        }
        #line 1571
        nondet_res = __VERIFIER_nondet_int();
        #line 1573
        if (nondet_res == 1)
            lock_mutex_unlock_list_lock = 2;
        #line 1575
        return (0);
    }
    #line 1578
    return (-4);
}
}
#line 1583 "/work/vladimir/OPDS/Work/Kernel/current-3.10/drivers/usb/core/usbcore.h:8: default:3.2.3-0-32_7a-0-ufa/line:3.7.3: kind: bug: 201809: symbol: futex_wait_timeout(2, 0, common: mode/15a_common_mutex.c"
void lock_mutex_unlock_list_lock(struct mutex *lock)
{
    #line 1586
    if (!lock_mutex_unlock_list_lock == 1) {
        #line 1589
        } else {
            #line 1591
            lock_error();
        }
        #line 1593
    }
}
}
```

specification

software verifier

**SAFE**  
i.e., assertions  
cannot be violated

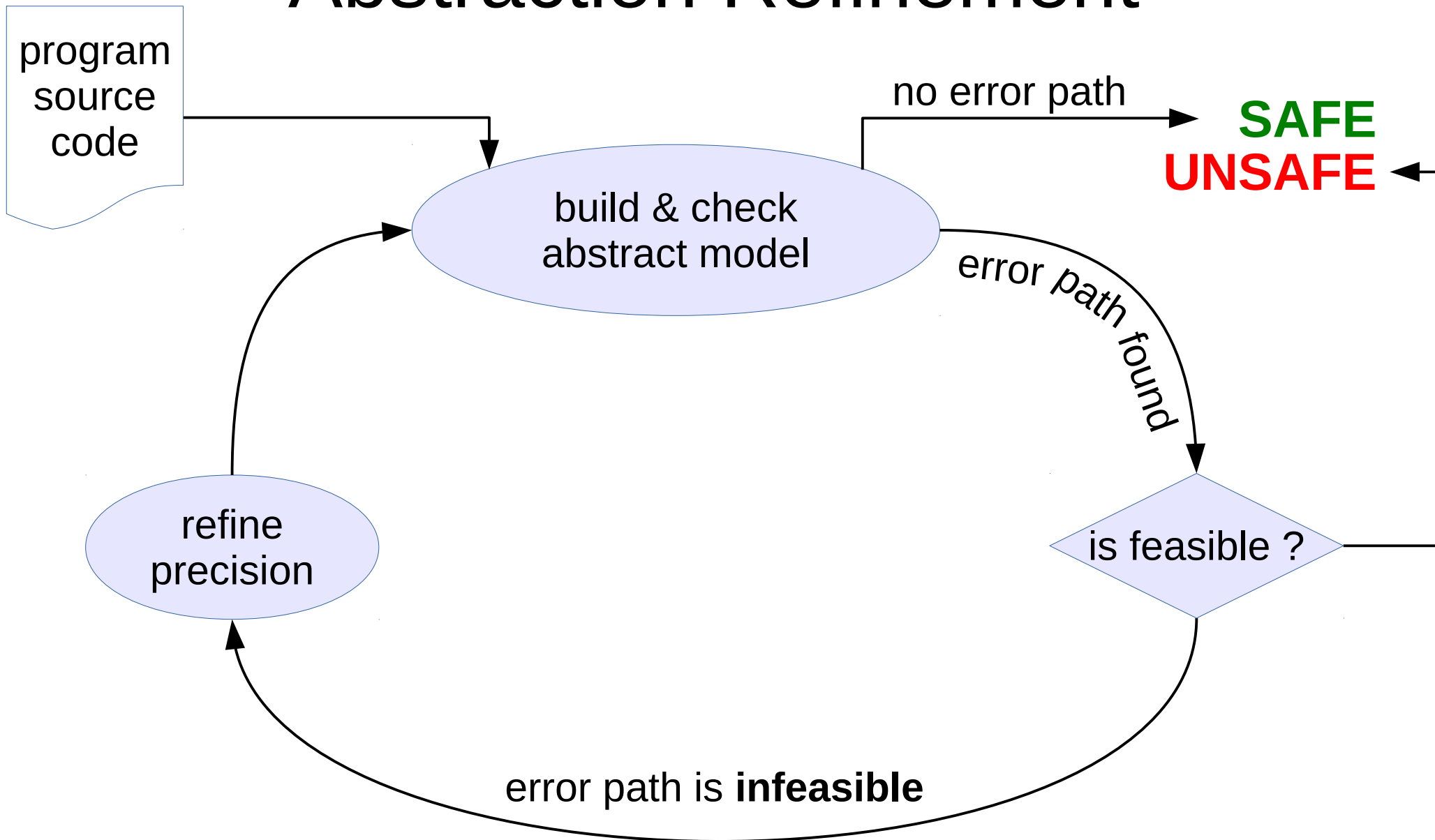
**UNKNOWN**  
timeout, memory out ...

**UNSAFE**  
i.e., there is a bug  
in the program

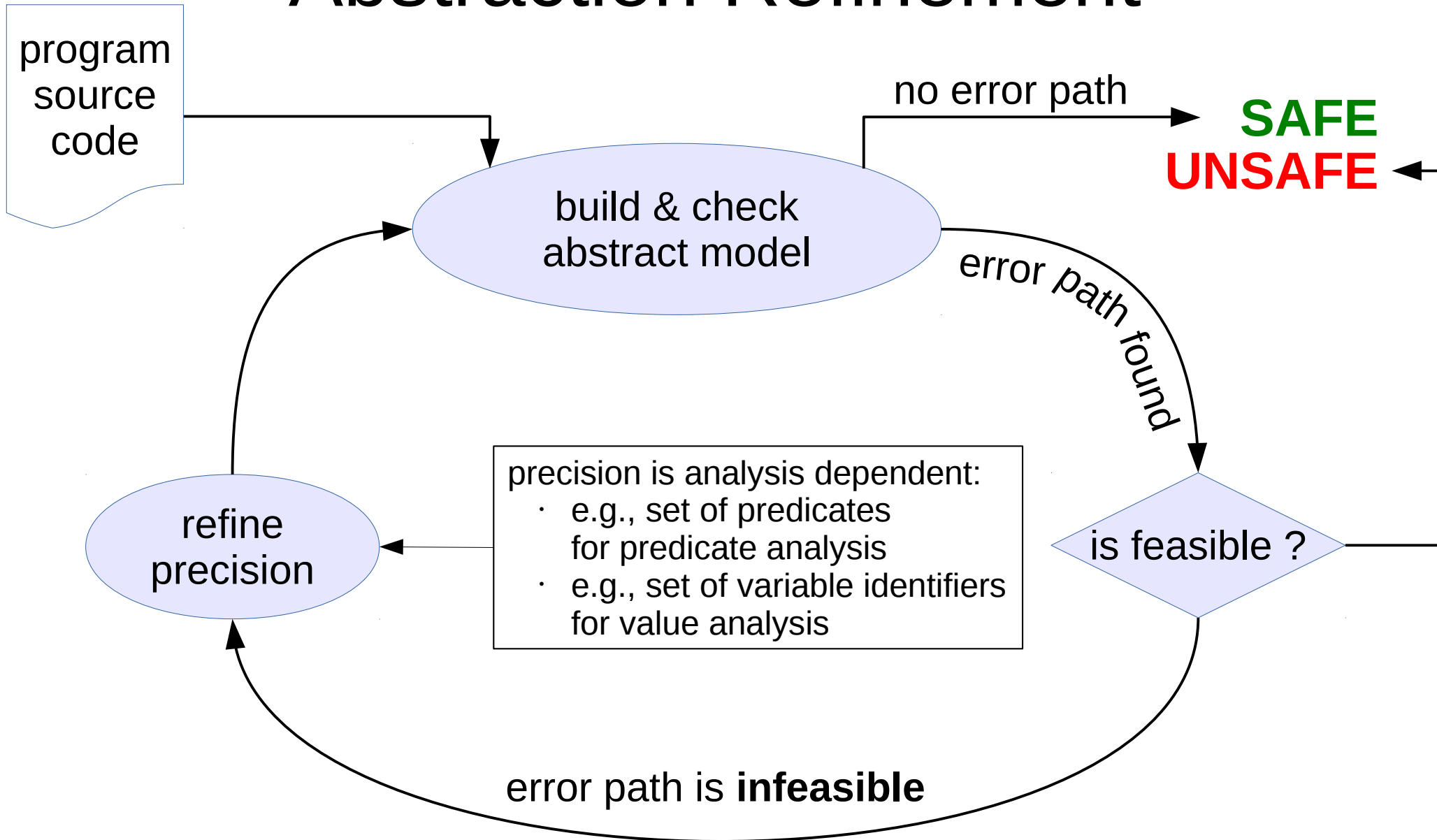
# Abstraction

- Disregard irrelevant information
  - Avoids state-space explosion
  - Allows verification of real-world software to scale
  - Success story of **SLAM** project at Microsoft
- But how does a **good** abstraction look like?
  - Too coarse → False alarms
  - Too precise → More timeouts
  - Impossible to do manually → Automation needed

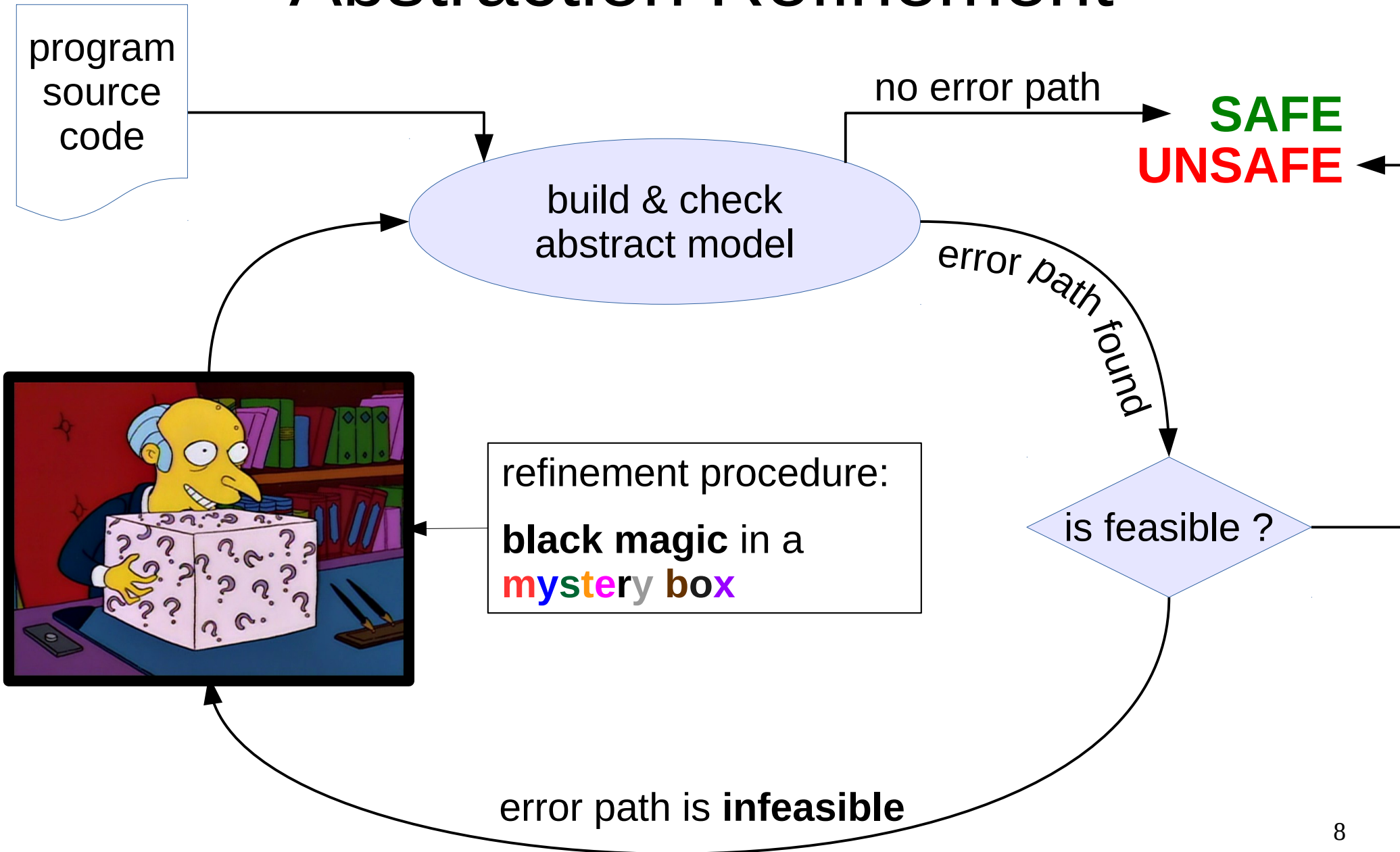
# Counterexample-Guided Abstraction Refinement



# Counterexample-Guided Abstraction Refinement

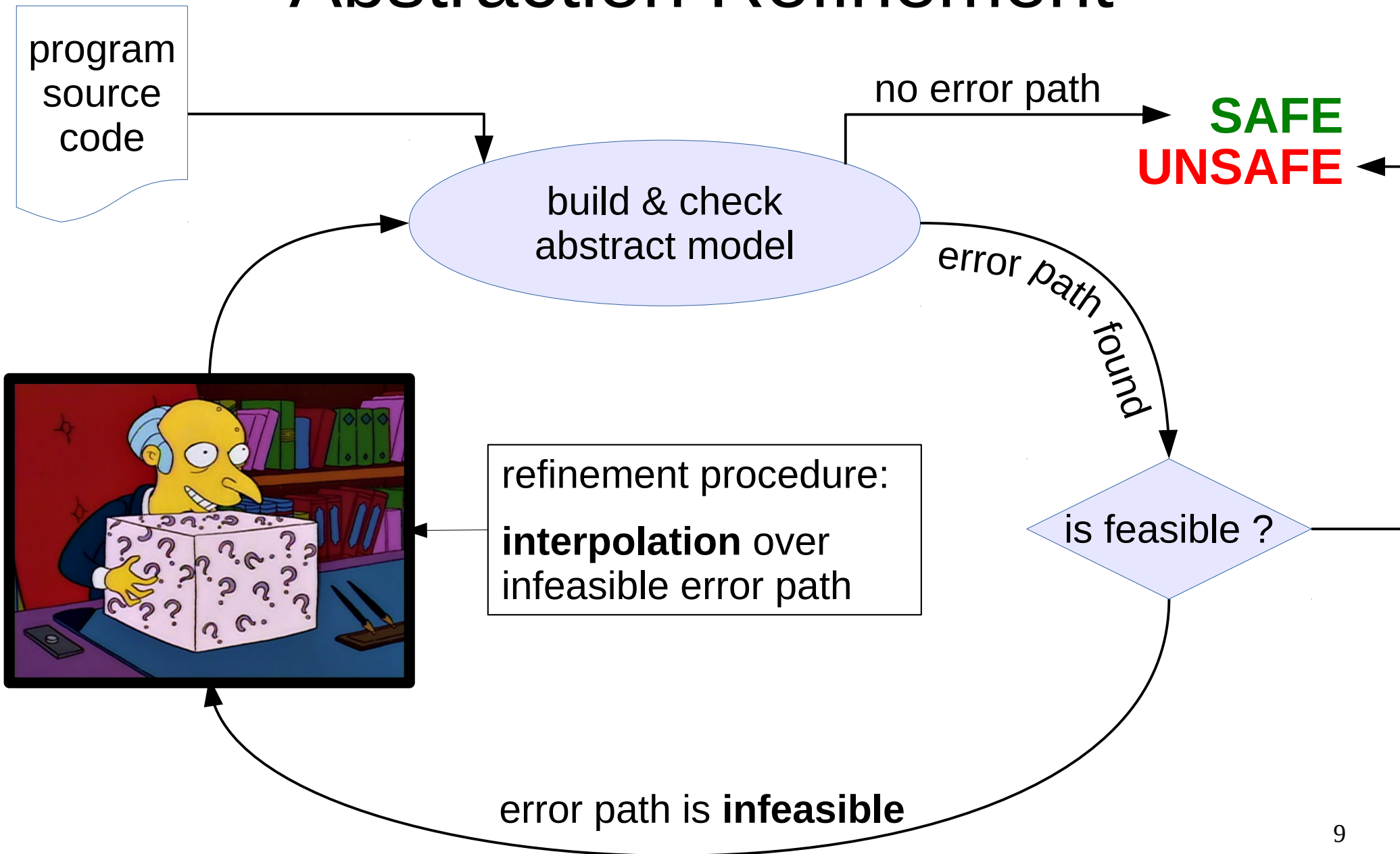


# Counterexample-Guided Abstraction Refinement

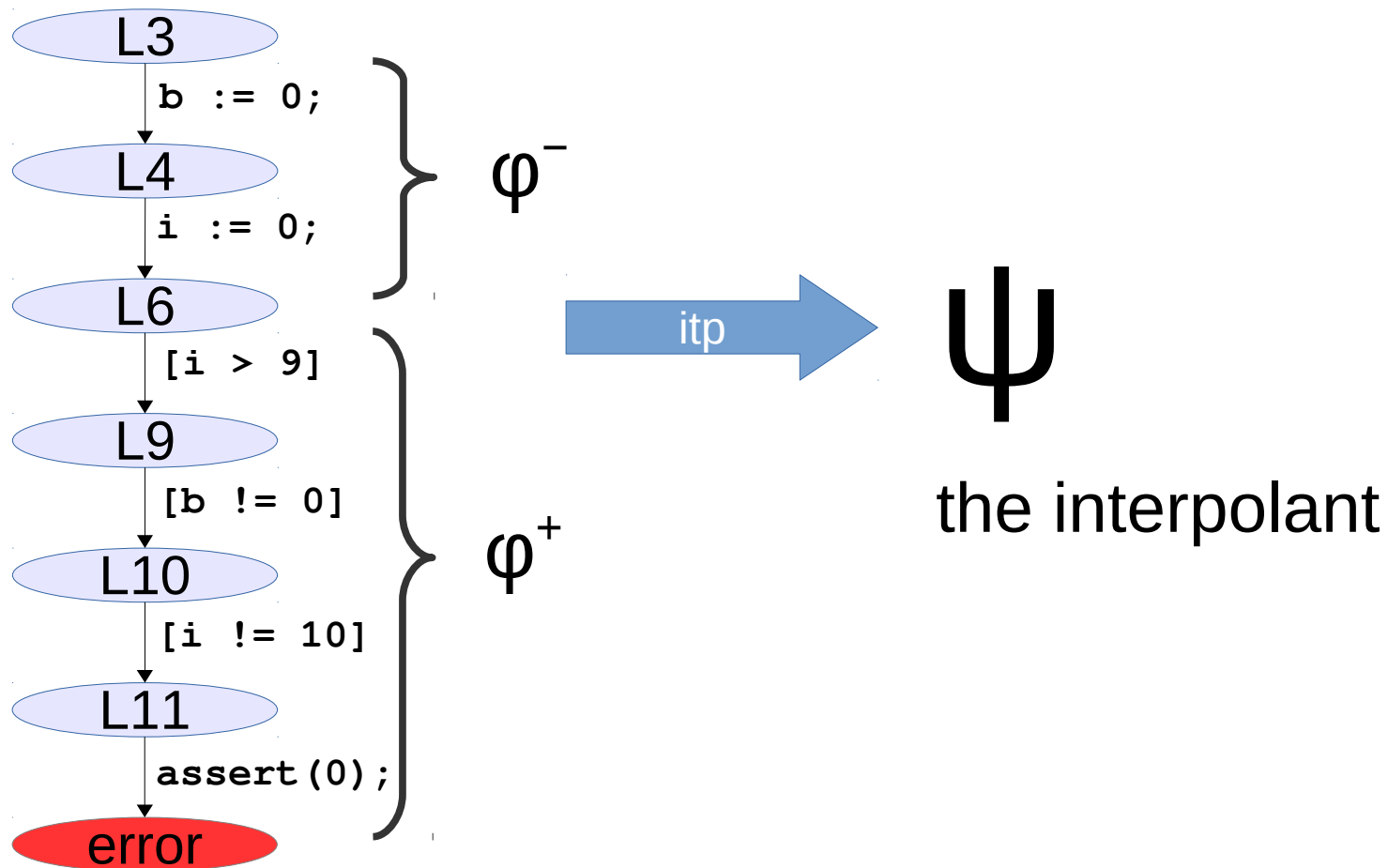




# Counterexample-Guided Abstraction Refinement



# Craig Interpolation



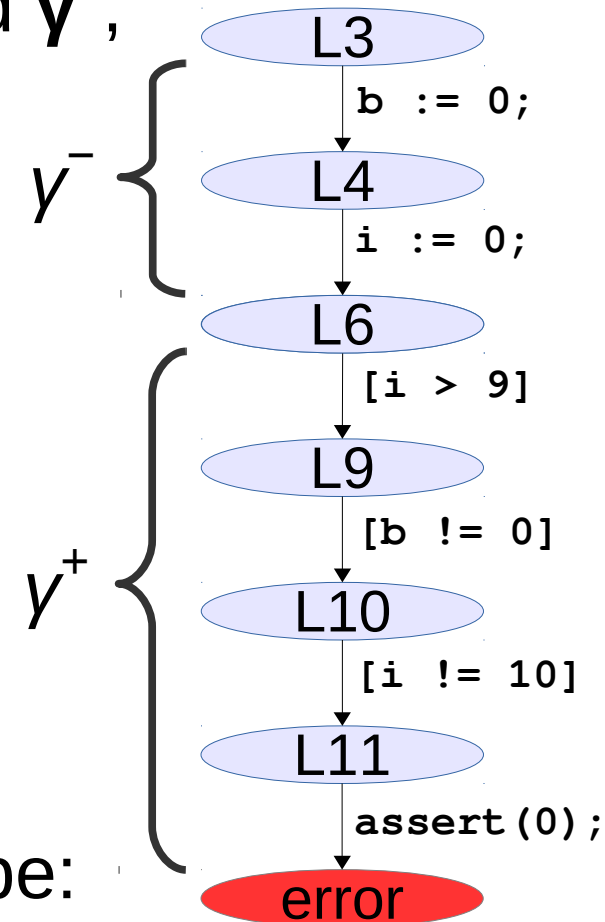
At L6 the interpolant  $\psi$  for  $\varphi^-$  and  $\varphi^+$  could be:  
[`b = 0`], or [`i = 0`], or [`b = 0  $\wedge$  i = 0`], or ...

# “Explicit-Value” Interpolation

For a pair of **constraint sequences**  $\gamma^-$  and  $\gamma^+$ , such that  $\gamma^- \wedge \gamma^+$  is **contradicting**, **interpolant**  $\psi$  is a **constraint sequence**  $\gamma^-$  that fulfills the following requirements:

- 1)  $\gamma^-$  implies  $\psi$
- 2)  $\psi \wedge \gamma^+$  is unsatisfiable
- 3)  $\psi$  only contains symbols that are common to both  $\gamma^-$  and  $\gamma^+$

At **L6** the interpolant  $\psi$  for  $\gamma^-$  and  $\gamma^+$  could be:  
[ $b = 0$ ], or [ $i = 0$ ], or [ $b = 0 \wedge i = 0$ ], or ...



# Interpolants

- Represent **concise** explanations for infeasibility of error
- Therefore, ideally suited for refinement of precision

# Interpolation

- Represent **concise** explanations for infeasibility of error
- ~~Therefore, ideally suited for refinement of precision~~
- **Theoretically**, well suited for refinement of precision

# Example – Good vs. Bad Interpolants

## Input Program

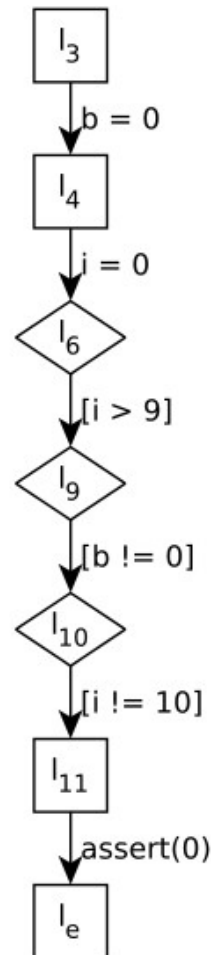
```
1  extern int f(int x);
2  int main() {
3      int b = 0;
4      int i = 0;
5      while(1) {
6          if(i > 9) break;
7          f(i++);
8      }
9      if(b != 0) {
10         if(i != 10) {
11             assert(0);
12         }
13     }
14 }
```

# Example – Good vs. Bad Interpolants

## Input Program

```
1  extern int f(int x);
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8      }
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13     }
14 }
```

## Abstract Error Path

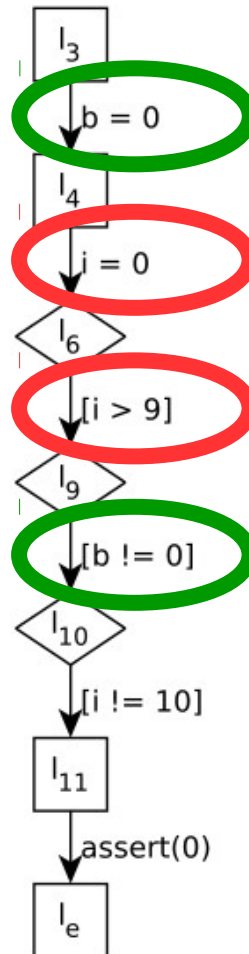


# Example – Good vs. Bad Interpolants

## Input Program

```
1  extern int f(int x);
2  int main() {
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10         if(i != 10) {
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```

## Abstract Error Path



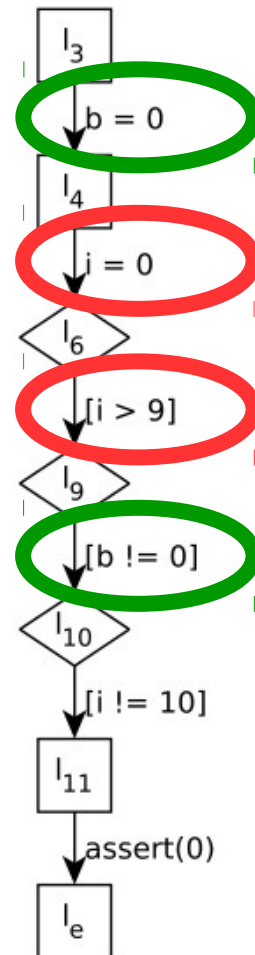


# Example – Good vs. Bad Interpolants

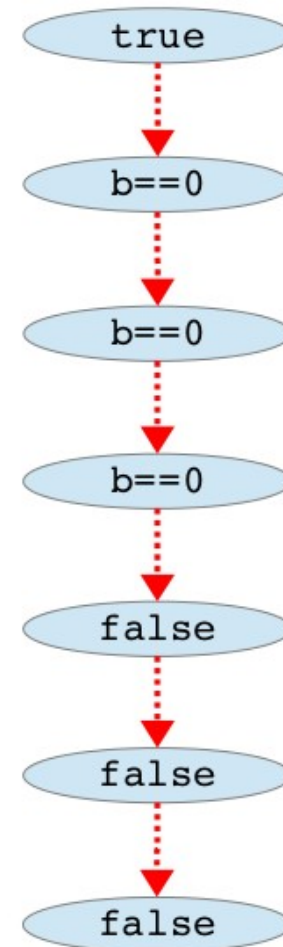
## Input Program

```
1  extern int f(int x);
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8    }
9    if(b != 0) {
10     if(i != 10) {
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12     }
13   }
14 }
```

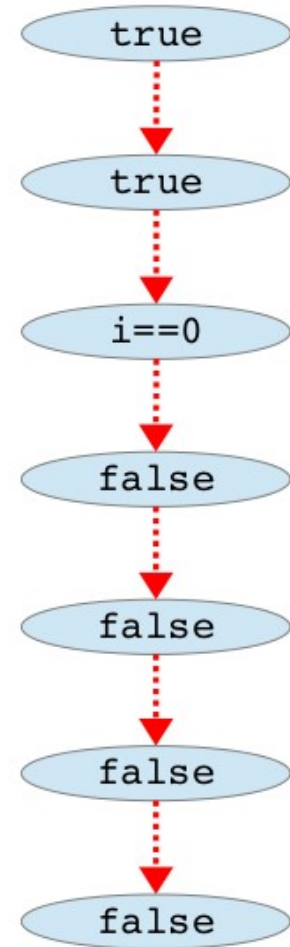
## Abstract Error Path



## Good Interpolants



## Bad Interpolants



# Example – Good vs. Bad Interpolants

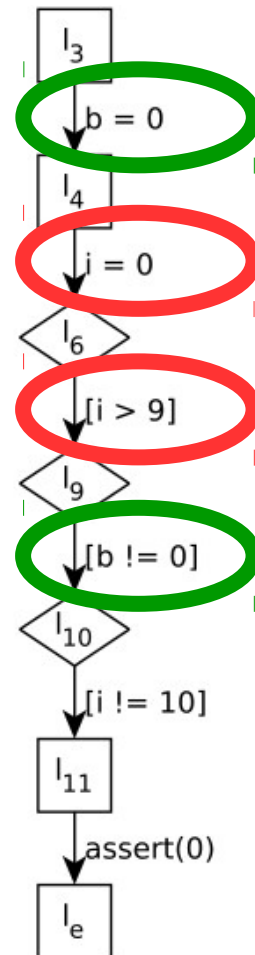
## Input Program

```

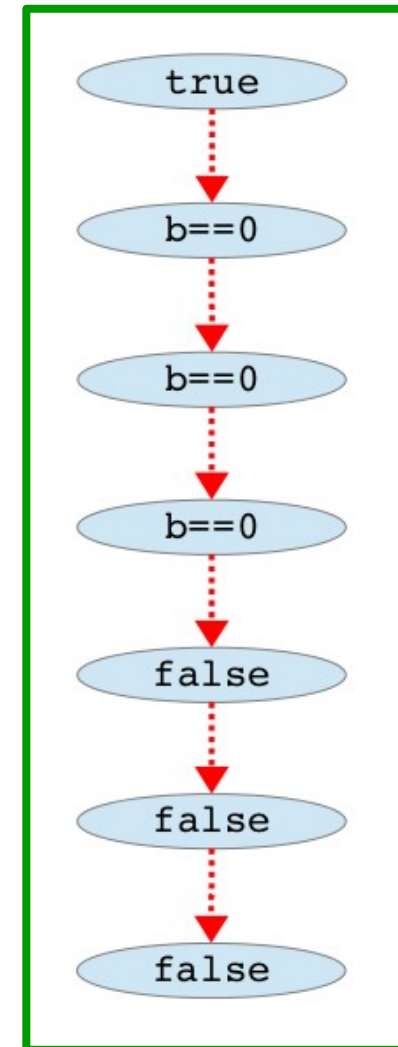
1  extern int f(int x);
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9      if(b != 0) {
10         if(i != 10) {
11             assert(0);
12         }
13     }
14 }

```

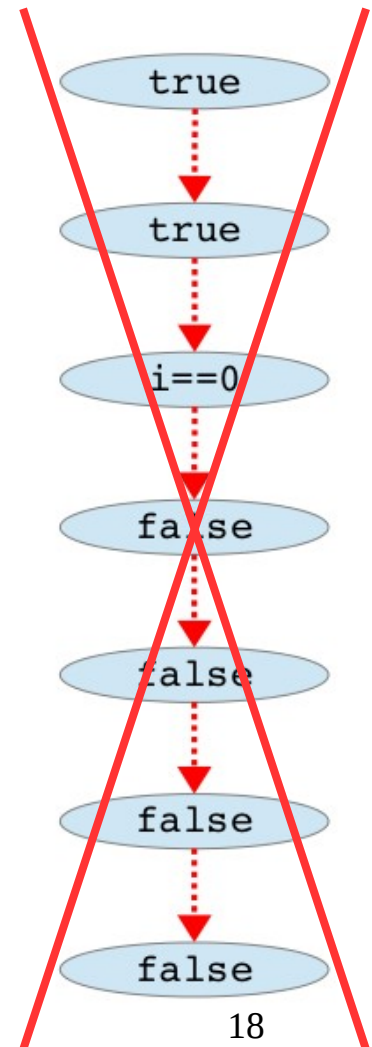
## Abstract Error Path



## Good Interpolants



## Bad Interpolants



# Interpolation – Recap

- Represent **concise** explanations for infeasibility of error
  - ~~Therefore, ideally suited for refinement of precision~~
  - **Theoretically**, well suited for refinement of precision
- Single interpolation problem typically has several solutions
  - Which interpolant do we get?
  - Some are “good”, others might lead to divergence
  - Selection controlled by internals of interpolation engine

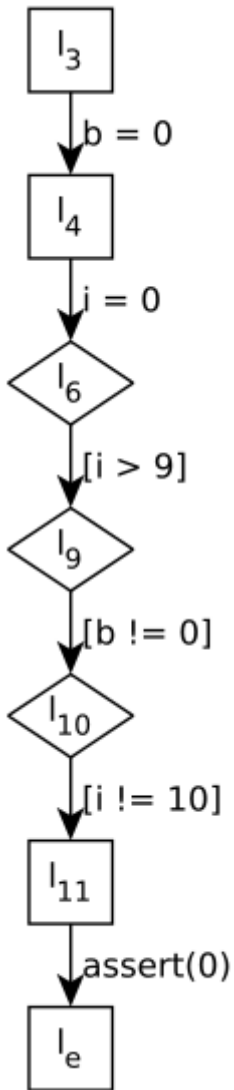
# Interpolation – Guided

- Represent **concise** explanations for infeasibility of error
  - ~~Therefore, ideally suited for refinement of precision~~
  - **Theoretically**, well suited for refinement of precision
- Single interpolation problem typically has several solutions
  - Which interpolant do we get?
  - Some are “good”, others might lead to divergence
  - Selection controlled by internals of interpolation engine
- Guide interpolation, ideally towards good interpolants



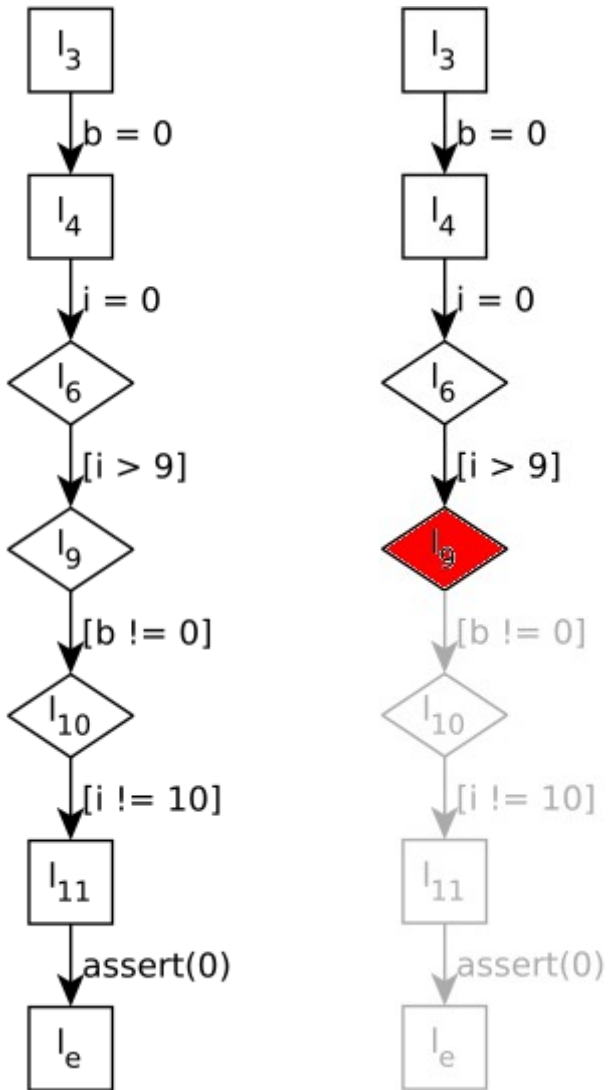
# Extraction of Infeasible Sliced Prefixes

## Abstract Error Path



# Extraction of Infeasible Sliced Prefixes

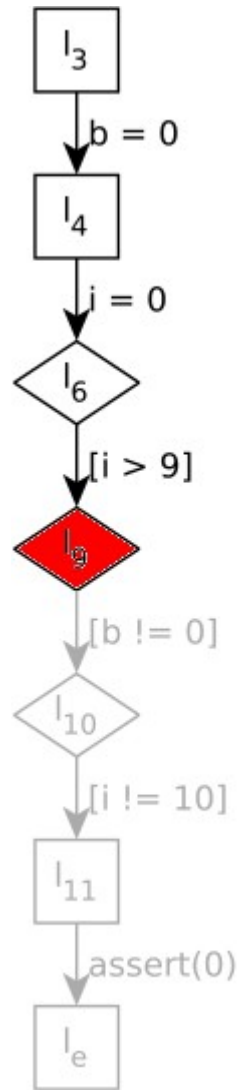
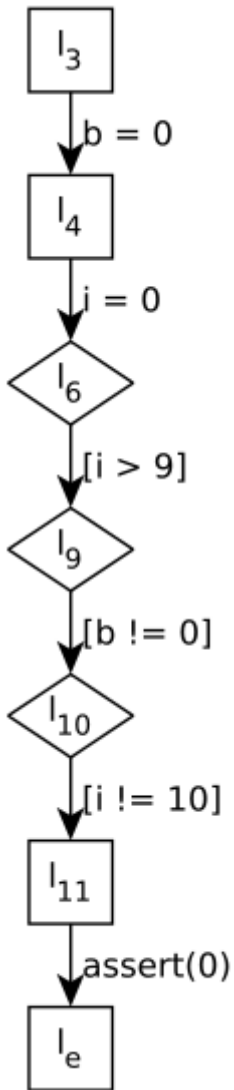
## Abstract Error Path



# Extraction of Infeasible Sliced Prefixes

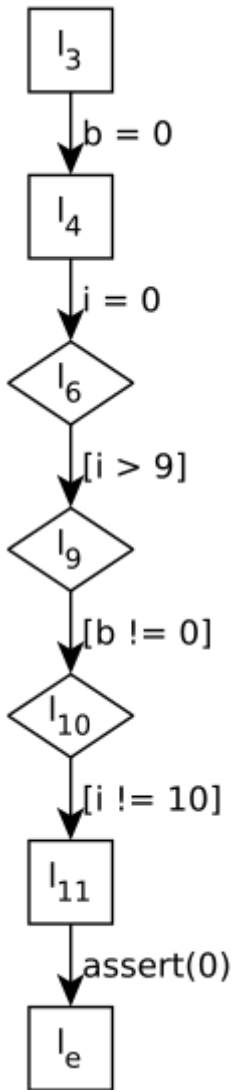
Abstract Error Path

1<sup>st</sup> Prefix

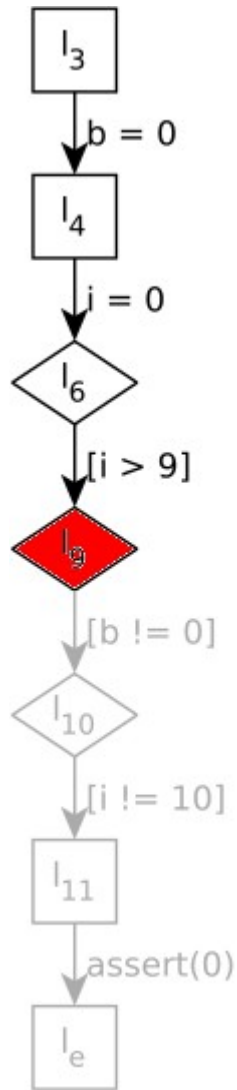


# Extraction of Infeasible Sliced Prefixes

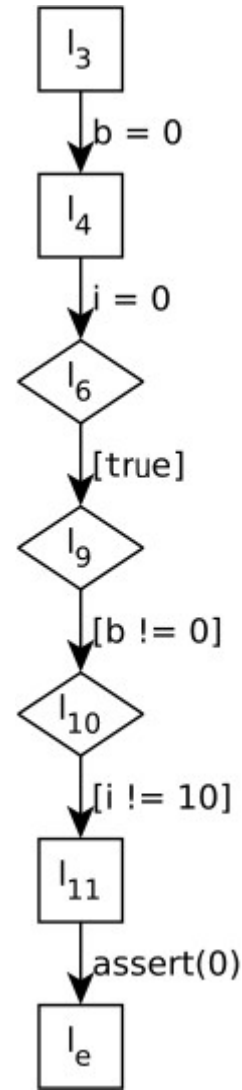
Abstract Error Path



1<sup>st</sup> Prefix



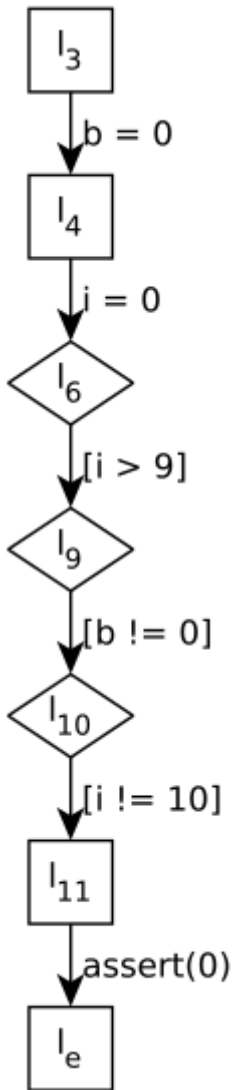
Sliced Error Path



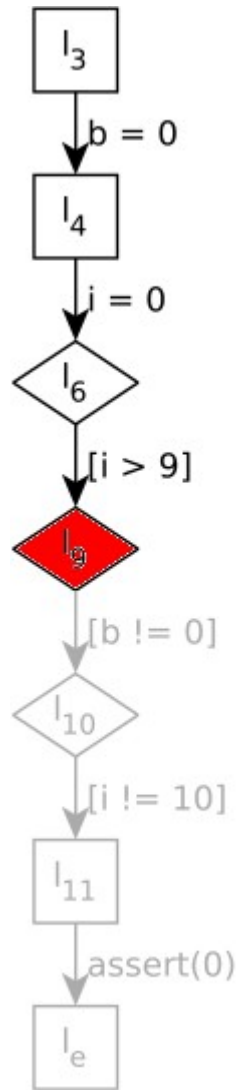


# Extraction of Infeasible Sliced Prefixes

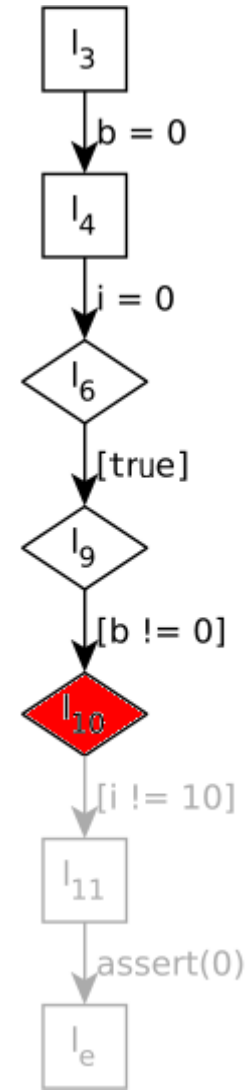
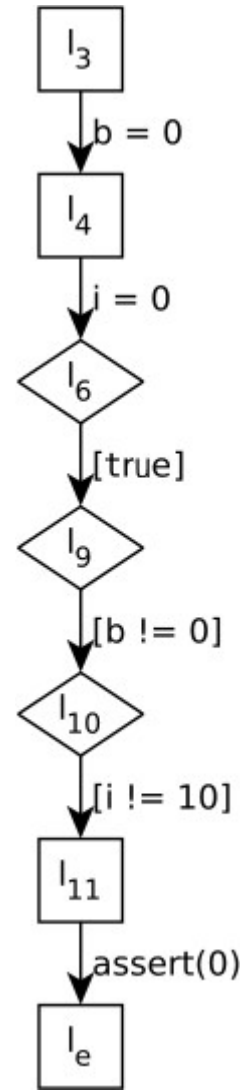
Abstract Error Path



1<sup>st</sup> Prefix

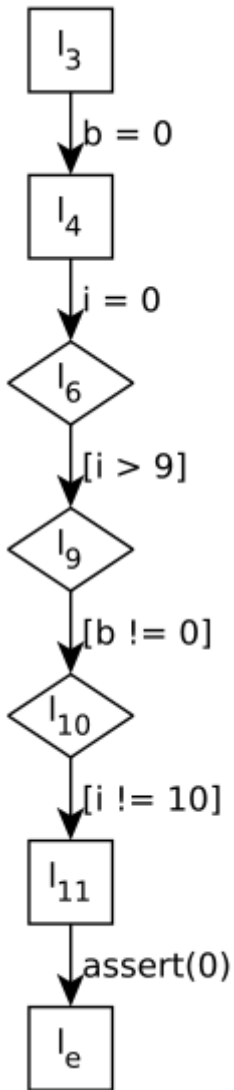


Sliced Error Path

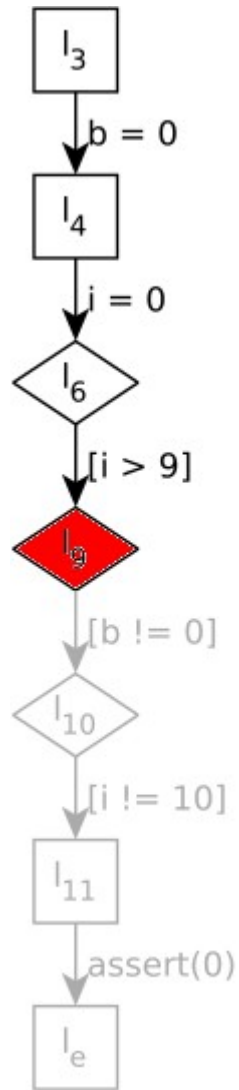


# Extraction of Infeasible Sliced Prefixes

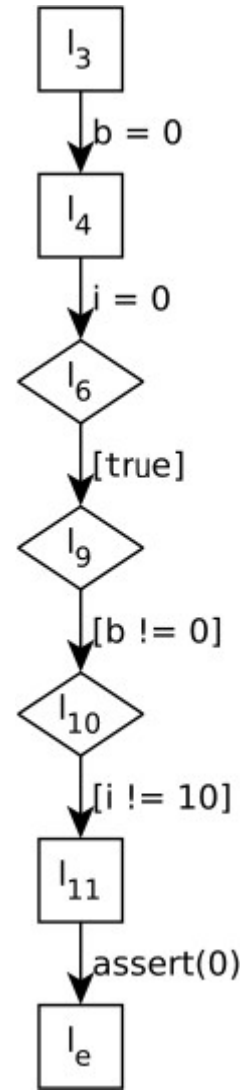
Abstract Error Path



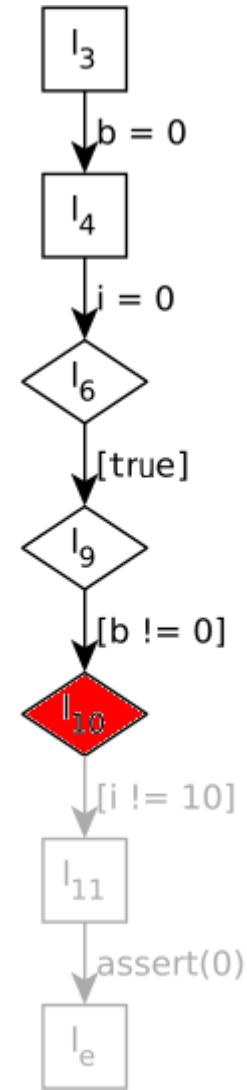
1<sup>st</sup> Prefix



Sliced Error Path

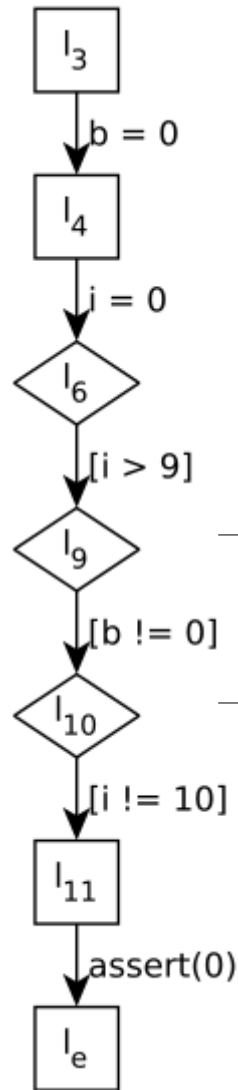


2<sup>nd</sup> Prefix

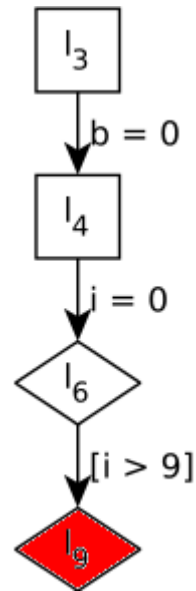


# Extraction of Infeasible Sliced Prefixes

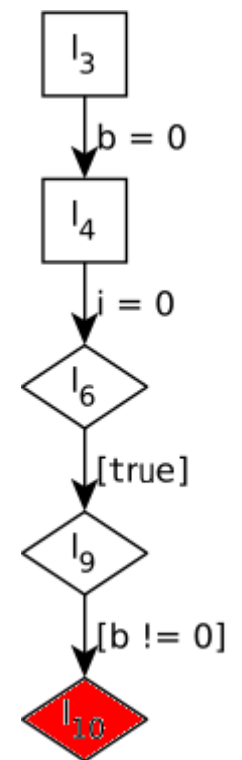
Single Abstract Error Path



1<sup>st</sup> Prefix

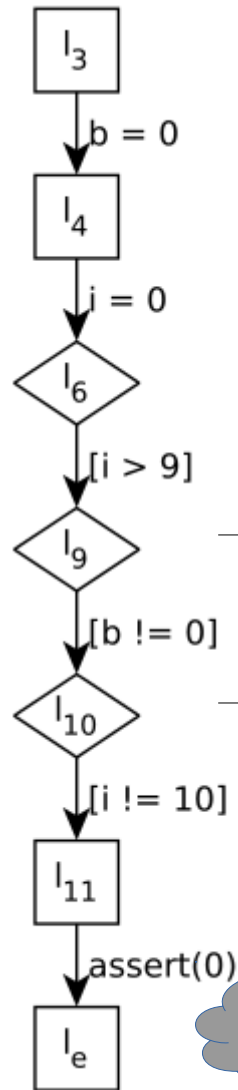


2<sup>nd</sup> Prefix

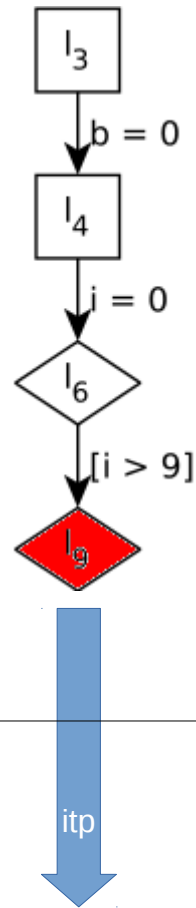


# Extraction of Infeasible Sliced Prefixes

Single Abstract Error Path

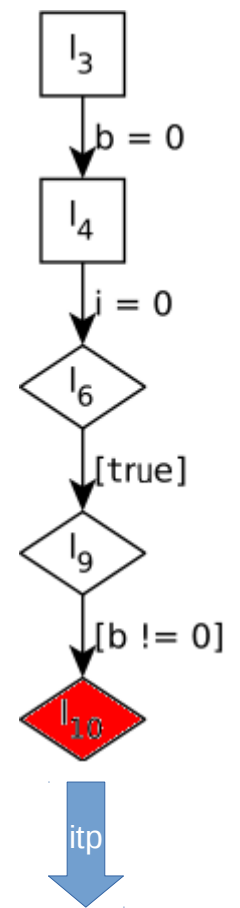


1<sup>st</sup> Prefix

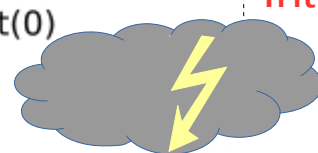


Interpolant sequence over loop-counter variable

2<sup>nd</sup> Prefix



Interpolant sequence over boolean variable



# Proposition: Interpolants of Prefixes

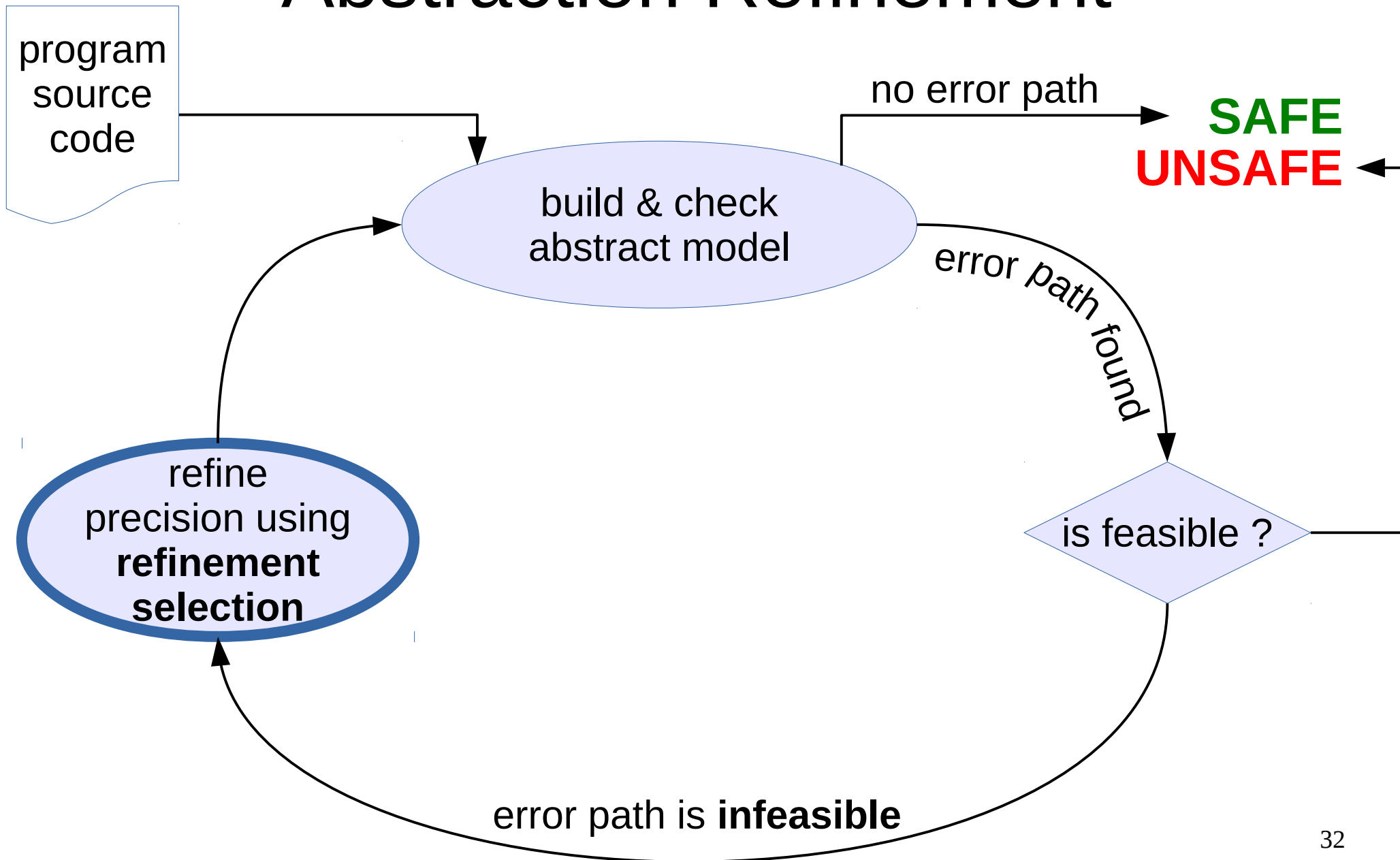
Any **infeasible sliced prefix  $\varphi$**   
that is extracted from an **infeasible error path  $\sigma$**   
can be used **for interpolation**  
to **exclude the original error path  $\sigma$**   
from **subsequent iterations** of CEGAR loop

# Proposition: Interpolants of Prefixes

Any **infeasible sliced prefix  $\varphi$**   
that is extracted from an **infeasible error path  $\sigma$**   
can be used **for interpolation**  
to **exclude the original error path  $\sigma$**   
from **subsequent iterations** of CEGAR loop

 **We can use any prefix we want for interpolation !**  
**refinement selection is now possible**

# Counterexample-Guided Abstraction Refinement



# Refinement Selection

- Refinement is now an optimization problem
- Select any refinement from a set of refinements
- Different heuristics for selecting an refinement
  - Shortest or longest prefix
  - Best or worst score based on Domain-Types
  - Width of precision
  - Depth of refinement root
  - ...



# Implementation & Experiments

- Evaluated on over 2500 benchmarks from SV-COMP'15
- Evaluated under rules of SV-COMP'15
  - 15 minutes CPU Time
  - 15 GB RAM
- All ideas and concepts described are implemented
  - Integrated into CPAchecker
  - Extended Value Analysis



<http://cpachecker.sosy-lab.org>

# Results

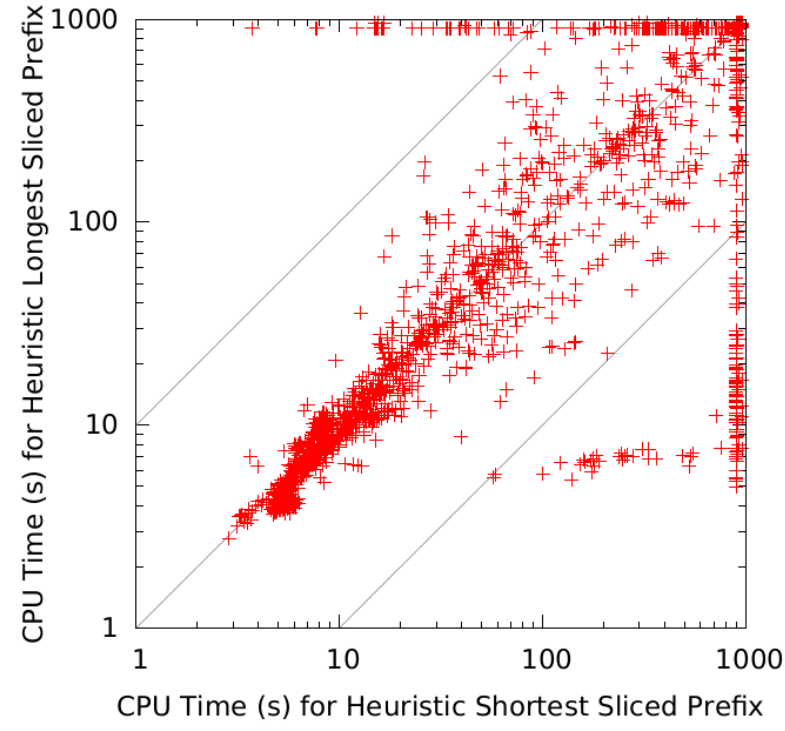
Heuristic	# Tasks	Sliced-Prefix Length		Score		Oracle		
		Shortest	Longest	Best	Worst	Best	Worst	Diff
DeviceDrivers64	619	326	395	399	319	403	315	88
ECA	1 140	489	512	570	478	611	410	201
ProductLines	597	456	361	402	360	463	353	110
Sequentialized	234	29	22	30	27	30	19	11
All Tasks	2 696	1 369	1 359	1 470	1 252	1 577	1 165	412

# Results

Heuristic	# Tasks	Sliced-Prefix Length		Score		Oracle		Diff
		Shortest	Longest	Best	Worst	Best	Worst	
DeviceDrivers64	619	326	395	399	319	403	315	88
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All Tasks	2 696	1 369	1 359	1 470	1 252	1 577	1 165	412

# Results – Sliced-Prefix Length

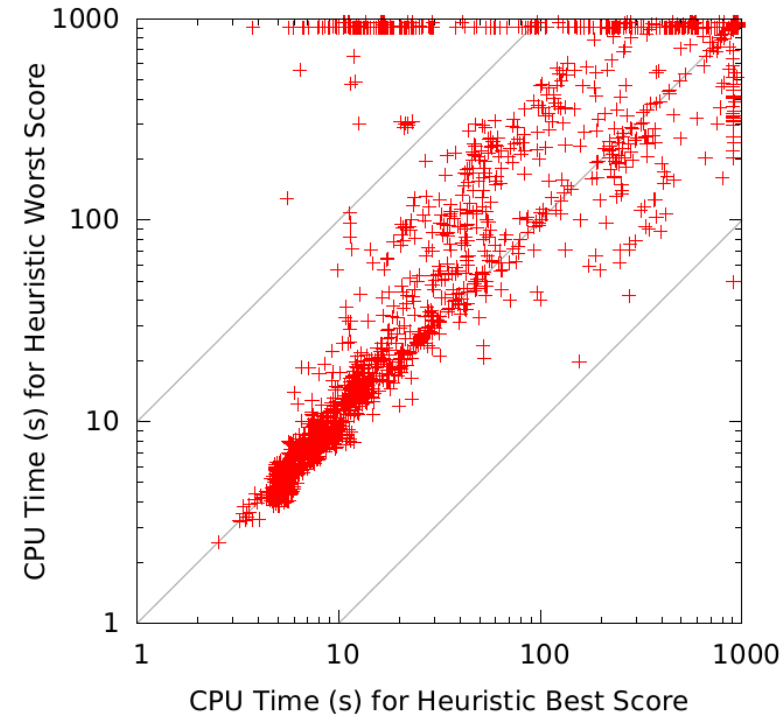
Heuristic	# Tasks	Sliced-Prefix Length	
		Shortest	Longest
DeviceDrivers64	619	326	395
ECA	1 140	489	512
ProductLines	597	456	361
Sequentialized	234	29	22
All Tasks	2 696	1 369	1 359



Heuristic “Shortest” vs. “Longest”

# Results – Domain-Type Score

Heuristic	# Tasks	Score	
		Best	Worst
DeviceDrivers64	619	399	319
ECA	1 140	570	478
ProductLines	597	402	360
Sequentialized	234	30	27
All Tasks	2 696	1 470	1 252



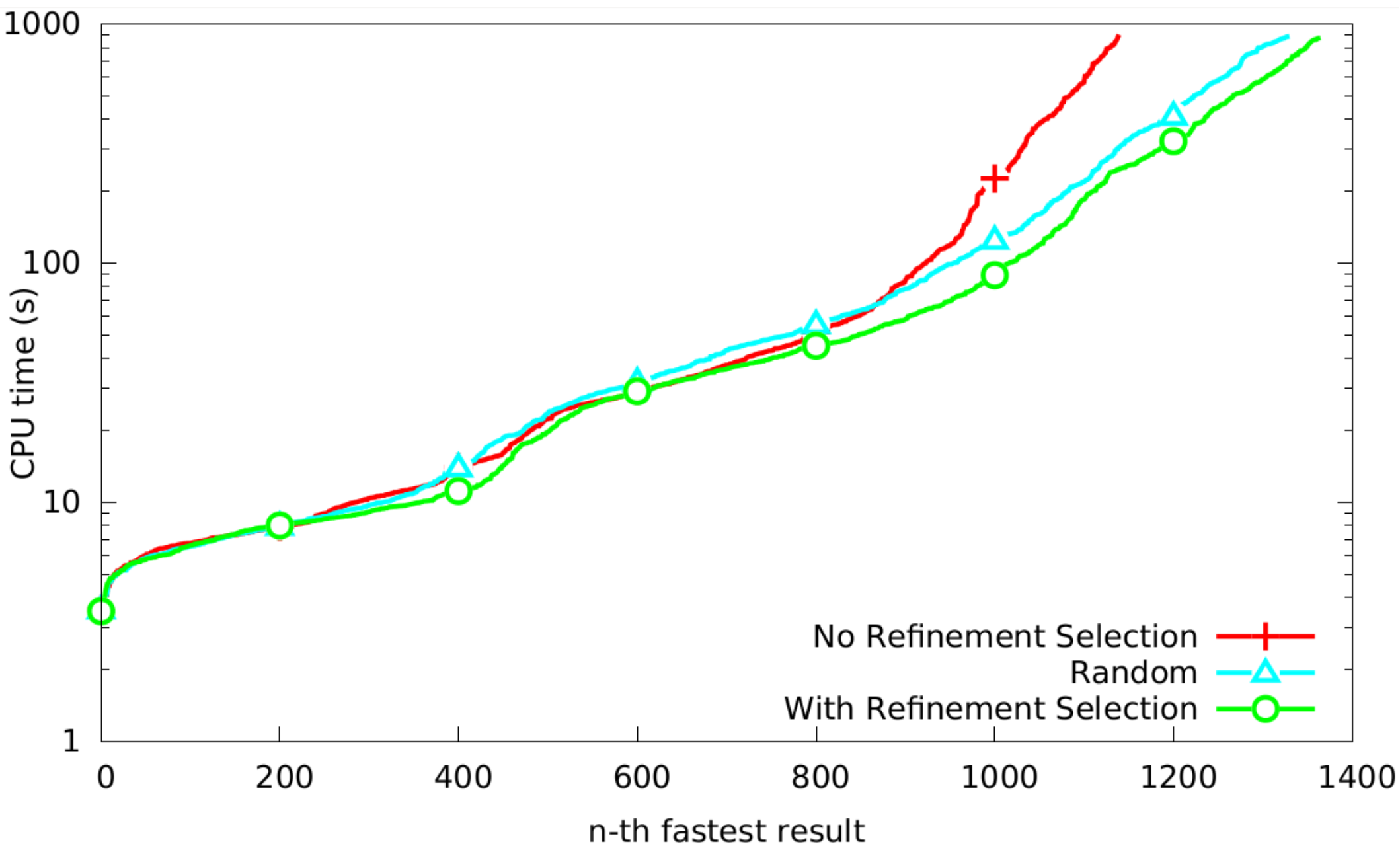
Heuristic “Best Score” vs. “Worst Score”

**We want Refinement Selection !!!**  
**Because straight-forward interpolation**  
**completely and utterly sucks !!!11!**

Stefan Löwe



# Now also for Predicate Abstraction!



# Conclusion

- Defined and implemented for two domains
  - infeasible sliced prefixes
  - precision selection heuristics
- Enables refinement selection
- Nice Results
  - Refinement selection matters!
  - More research needed on how to select refinements



<http://cpachecker.sosy-lab.org>