

Decision Heuristics and Restarts in SAT

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[SAT'15] *with Andreas Fröhlich*

- Evaluating CDCL Variable Scoring Schemes
 - attempt to simplify and explain decision heuristics in SAT
 - shows that VMTF **is** as good as VSIDS (and explains both)

[POS'15] *with Andreas Fröhlich*

- Evaluating CDCL Restart Schemes
 - simplifies the state-of-the-art Glucose style restart scheme
 - evaluation shows clear benefits but also weaknesses

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- (E)VSIDS decision heuristic
 - (Exponential) Variable State Independent Decaying Sum (VSIDS)
 - empirically one of the **most important features** of state-of-the-art solvers
 - no formal argument “why it works”
- reconsider simpler alternatives
 - particularly variable move to front schemes (VMTF)
 - requires careful data structure design
 - formalization of these heuristics
 - empirical evaluation
- so a step towards “Trying to Understand the Power of VSIDS”
 - ... and thus “**Why** CDCL Works”

original longer slide set at <http://fmv.jku.at/biere/talks/Biere-SAT15-talk.pdf>

- decision heuristics consist of
 - variable selection: which variable to assign next?
 - phase selection: assign variable to which phase (true or false)?
- phase saving [PipatsrisawatDarwiche'07]
 - select phase to which variable was assigned before
 - initialized by static one-side heuristics [JeroslowWang'90]
 - very effective and thus default in state-of-the-art solvers
- we consider only variable selection as decision heuristic here
 - clause based heuristics less effective (BerkMin, CMTF)
 - same applies to literal based heuristics (using literal scores)
- variable selection and **decision heuristic** boils down to
 - compute and maintain heuristic scores for variables
 - **select variable with highest score**

- how to compute scores
 - static or dynamic
 - **bump** variables: when to increase scores and by how much
 - **rescore** variables: when to decrease scores and by how much
 - state-of-the-art: VSIDS (from Chaff)
more precisely the exponential variant (EVSIDS) of MiniSAT!
- data structures for finding decision variables
 - eager or lazy update of “order”
 - state-of-the-art: priority queue of variables ordered by score (MiniSAT)
- data structure depends on how scores are computed and vice versa

- zero order scheme = static scores
 - computed for instance once during preprocessing
 - still needs search for “best” unassigned variable
 - only total orders considered so far
- first order schemes = dynamic but state less
 - for instance: $\text{score} = \text{pos occs} \times \text{neg occs}$
 - independent of how search reached current branch / search node
 - might be quite expensive to compute / update (linear in CNF size)
- second order schemes: variable score depends on history of search
 - first order + learning \Rightarrow second order
 - but can also be used to speed up search for “best” variable
 - goal is logarithmic or even constant algorithm for variable selection

- VSIDS appeared in seminal Chaff paper from Princeton (2001)
- bump variables occurring in learned clauses
 - bumping means incrementing an integer VSIDS score
 - current state-of-the-art: bump all variables used to derive learned clause
 - independent of state of clauses (satisfied or not)
- rescoring gives focus on recently used variables
 - scores are “decayed”, e.g., originally divided by two every 256th conflict
 - “low pass filter” on “use frequency” of variables
 - video <http://youtu.be/MOjhFywLre8>
- search for next unassigned variable with largest score
 - keeps an array of variables sorted by score
 - only re-sorts it w.r.t. score during rescoring (every 256th conflict)
 - uses right-most unassigned variable, thus original implementation **imprecise**

- Siege SAT solver [Ryan'04] used variable move to front (VMTF)
 - bumped variables moved to head of doubly linked list
 - search for unassigned variable starts at head
 - variable selection is an **online sorting algorithm of scores**
 - classic “move-to-front” strategy achieves good amortized complexity
- original implementation severely restricted
 - only moved a subset of bumped variables
 - details about caching the search not described
no source code published either
 - not exactly the same as VSIDS
- as consequence VMTF not used in state-of-the-art solvers

- **floating point scores**
 - allows fine grained rescore at every conflict
 - consider multiplying by $f = 0.9$ every score at each conflict
- actually, instead of updating scores of all variables (at every conflict)
 - only increase score of bumped variables by g^i
 - with i the conflict-index, and $g = 1/f$
 - non-bumped variables not touched
- priority queue of variables ordered by score
 - implemented as **binary heap** with update (bump and bubble up)
 - lazy assigned variable removal
 - remove largest score variable from heap until unassigned one found
 - put unassigned variables not on the heap back (logarithmic complexity)
- normalized VSIDS (NVSIDS) $\in [0, 1]$ as (theoretical) model [Biere'08] + video

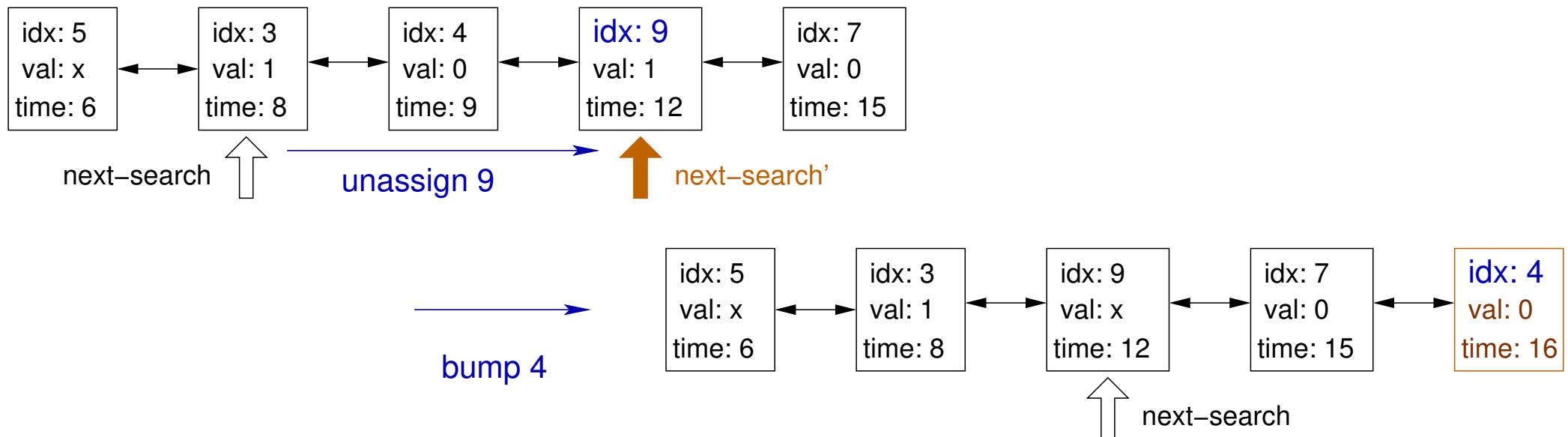
s old score s' new score

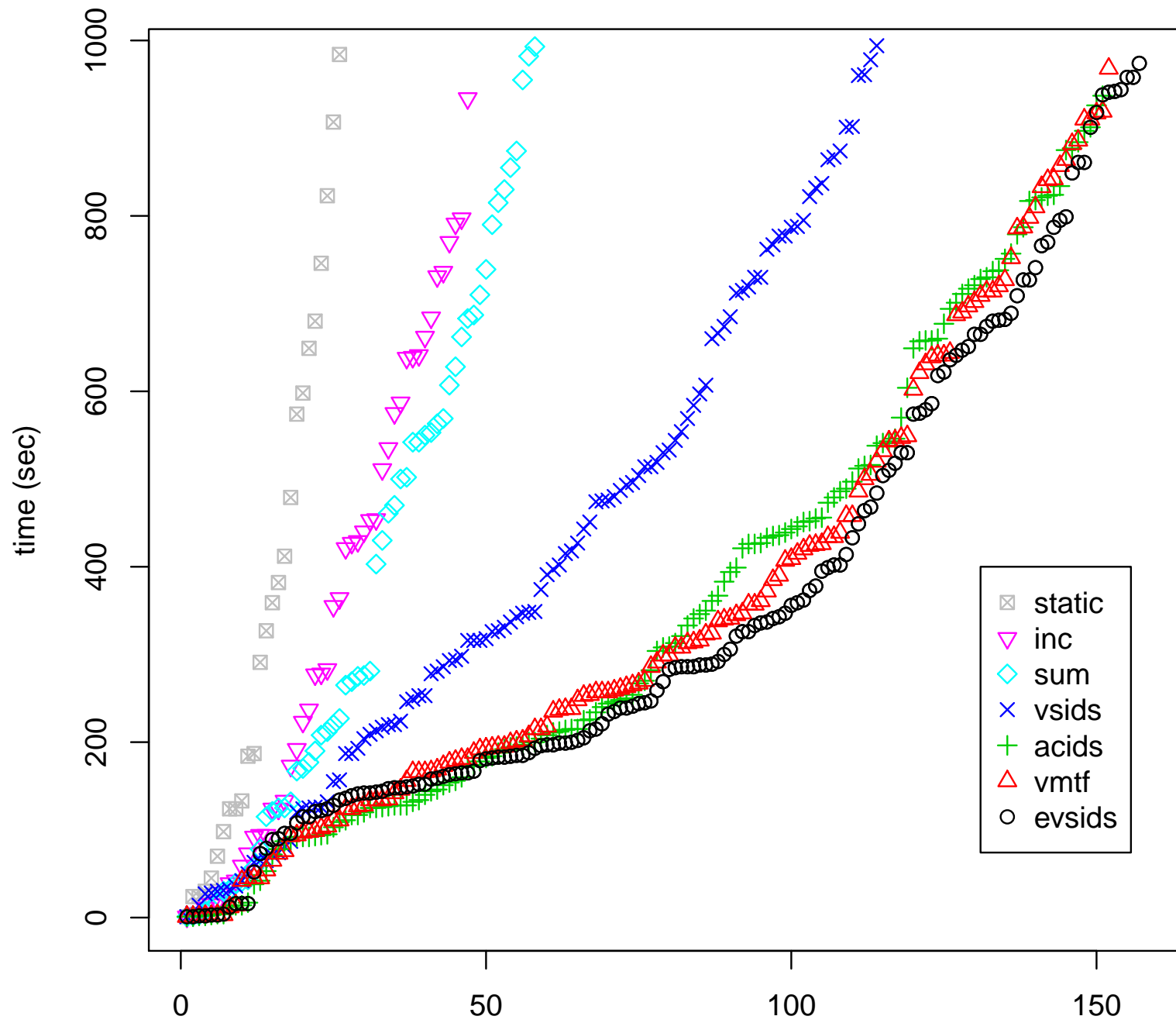
	variable score s' after i conflicts		
	bumped	not-bumped	
STATIC	s	s	static decision order
INC	$s + 1$	s	increment scores
SUM	$s + i$	s	sum of conflict-indices
VSIDS	$h_i^{256} \cdot s + 1$	$h_i^{256} \cdot s$	original implementation in Chaff
NVSIDS	$f \cdot s + (1 - f)$	$f \cdot s$	normalized variant of VSIDS
EVSIDS	$s + g^i$	s	exponential MiniSAT dual of NVSIDS
ACIDS	$(s + i)/2$	s	average conflict-index decision scheme
VMTF	i	s	variable move-to-front
VMTF'	b	s	variable move-to-front variant

$$0 < f < 1 \quad g = 1/f \quad h_i^m = 0.5 \quad \text{if } m \text{ divides } i \quad h_i^m = 1 \text{ otherwise}$$

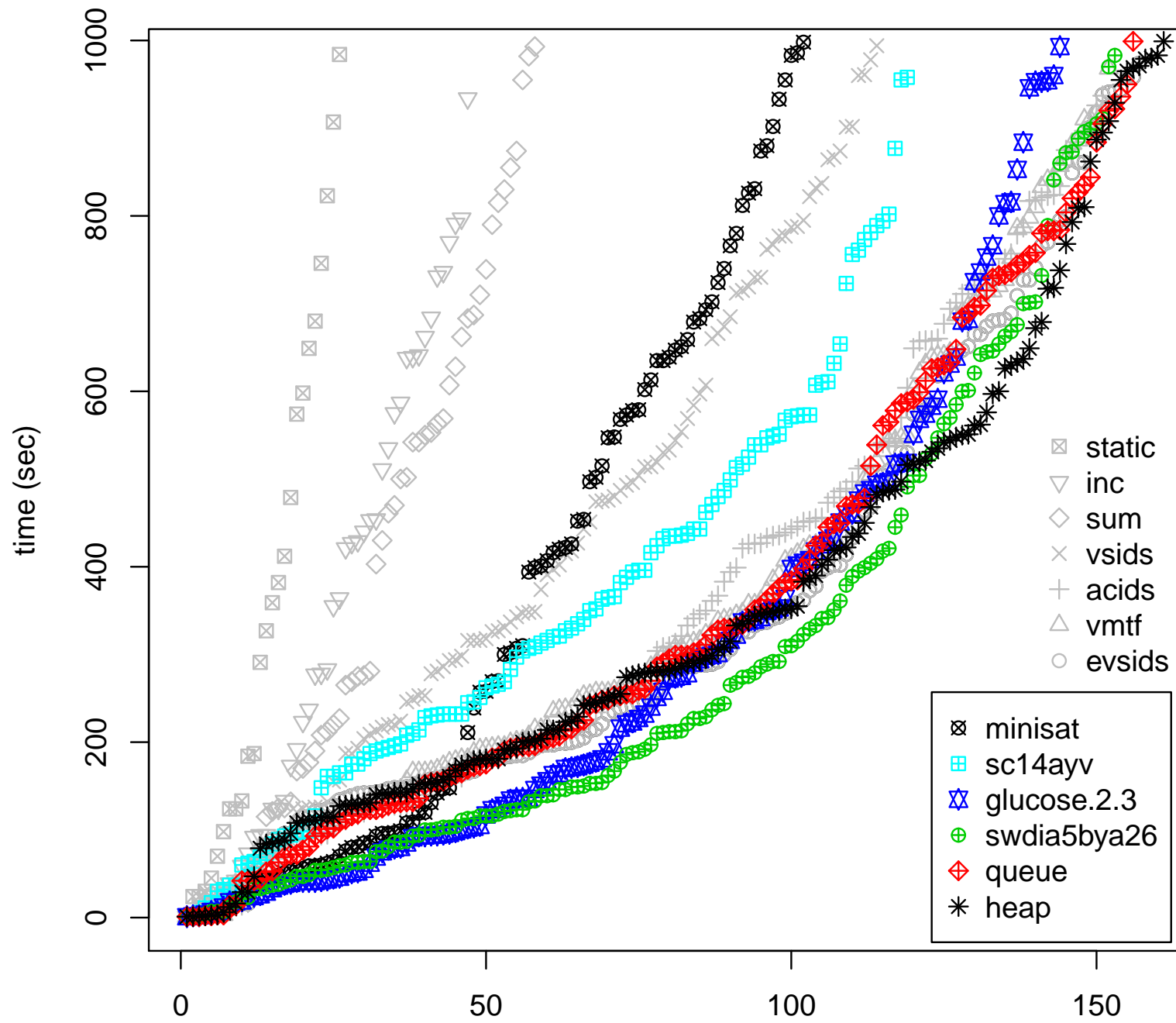
i conflict index b bumped counter

- fast simple implementation for caching searches in VMTF
 - doubly linked list does not have positions as an ordered array
 - bump = move-to-front = dequeue then insertion at the head
- time-stamp list entries with insertion time
 - maintained invariant: **all variables right of next-search are assigned**
 - requires update to next-search while unassigning variables

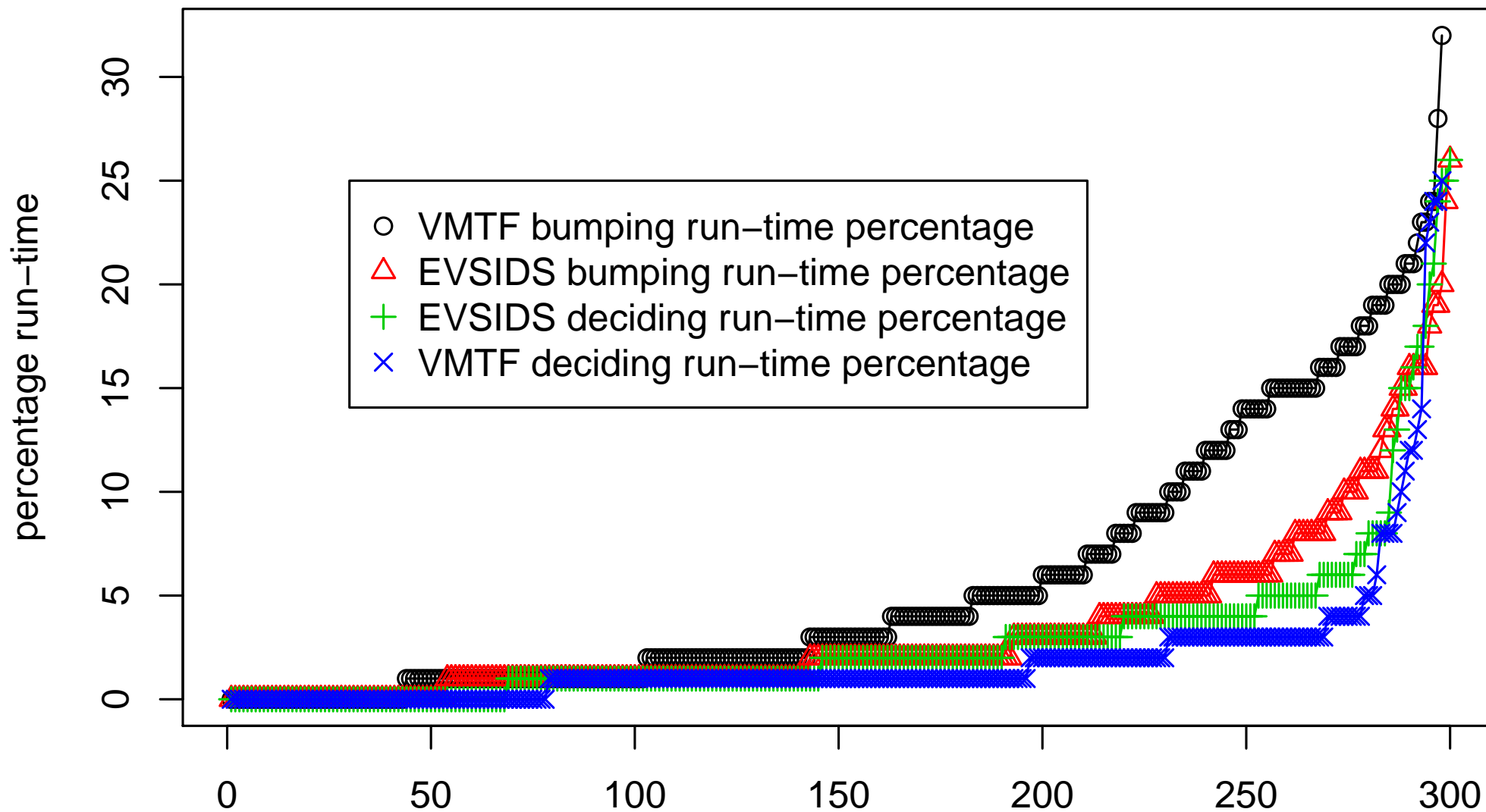




solved SAT competition 2014 application track instances (ordered by time)



solved SAT competition 2014 application track instances (ordered by time)



SAT'16 Competition Application Benchmarks (sorted by percentage run-time)

- surveyed and classified variable selection / scoring schemes
 - came up with a new one ACIDS (as well as SUM)
 - **EVSIDS, VMTF, ACIDS comparable in performance**
 - with a generic fast queue implementation
- VMTF was considered to be obsolete
 - can be made effective (with less code than EVSIDS)
 - needs proper optimized implementation: time-stamping with insertion-time
 - VMTF might be easier to reason about in proof complexity
- threads to validity
 - unclear whether VMTF **only** works in combination with Glucose restarts
see also our POS'15 paper and talk in Part II of this talk
 - benchmark selection in recent SAT competitions highly controversial
- SplatZ: SAT solver only based on VMTF

<http://fmv.jku.at/splatz>

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- Evaluating CDCL Restart Schemes
 - simplifies the state-of-the-art Glucose style restart scheme
 - evaluation shows clear benefits but also weaknesses

- Lingeling actually barely won
 - only for long time limit of 5000 seconds
 - for 900 seconds: no chance
- two main reasons
 - selected benchmark biased towards descendants of Glucose / MiniSAT
 - but Glucose restarts are important for many (selected) benchmarks
- the POS'15 paper is about lessons learned while
 - porting the Glucose restart scheme to Lingeling
 - and **simplifying** by
 - using exponential moving averages (*EMA*)

original longer slide set at <http://fmv.jku.at/biere/talks/Biere-POS15-talk.pdf>

SAT Competition 2014 application track instances clustered in **buckets** (by the organizers):

2d-strip-packing (4), argumentation (20), bio (11),

crypto-aes (8), crypto-des (7), crypto-gos (9),

crypto-md5 (21), [crypto-sha \(29\)](#), crypto-vpmc (4),

diagnosis (28), fpga-routing (1),

hardware-bmc (4), hardware-bmc-ibm (18), [hardware-cec \(30\)](#),

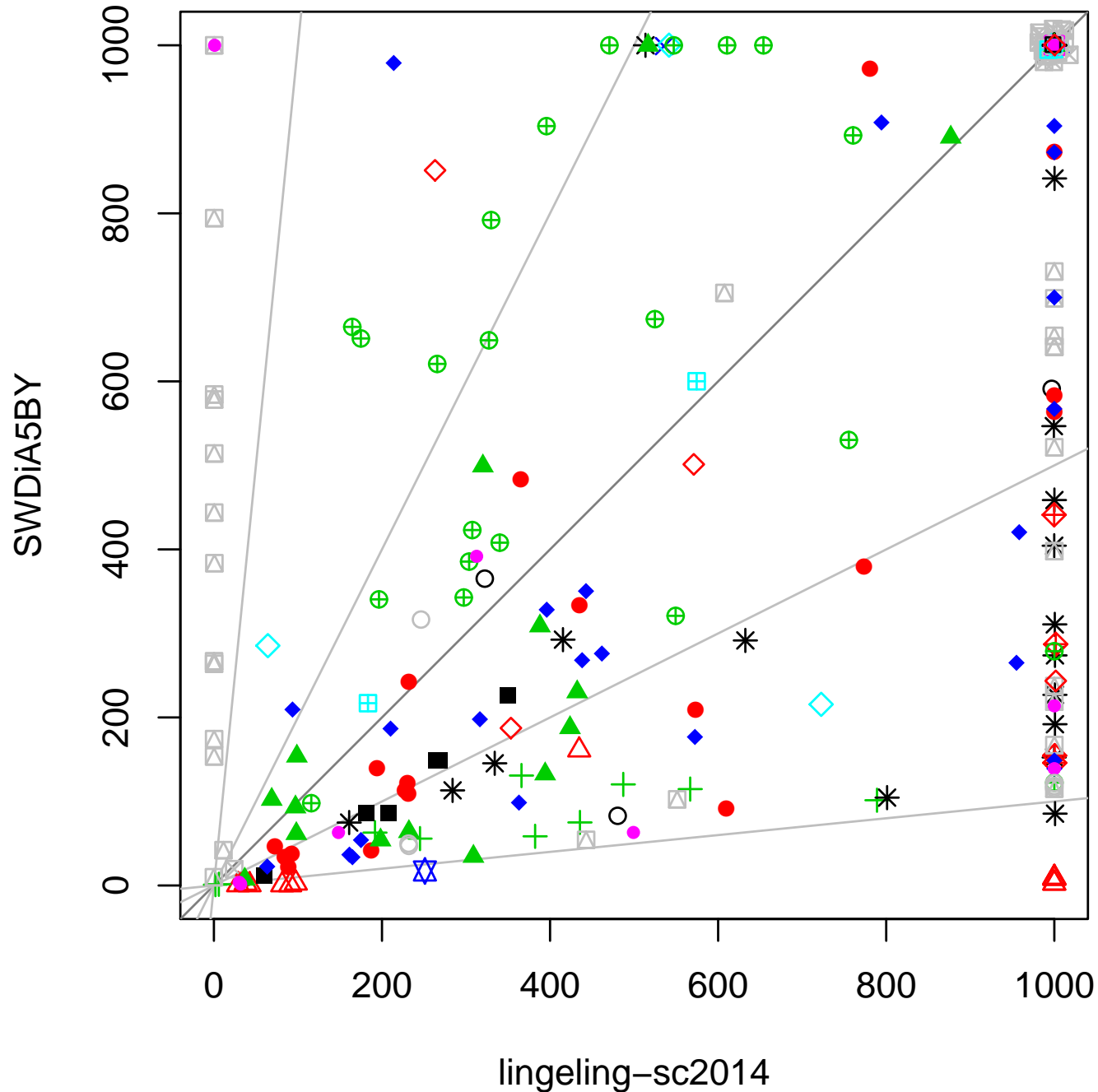
hardware-manolios (6), hardware-velev (27),

planning (19), scheduling (30), scheduling-pesp (3),

software-bit-verif (9), software-bmc (6), symbolic-simulation (1), termination (5)

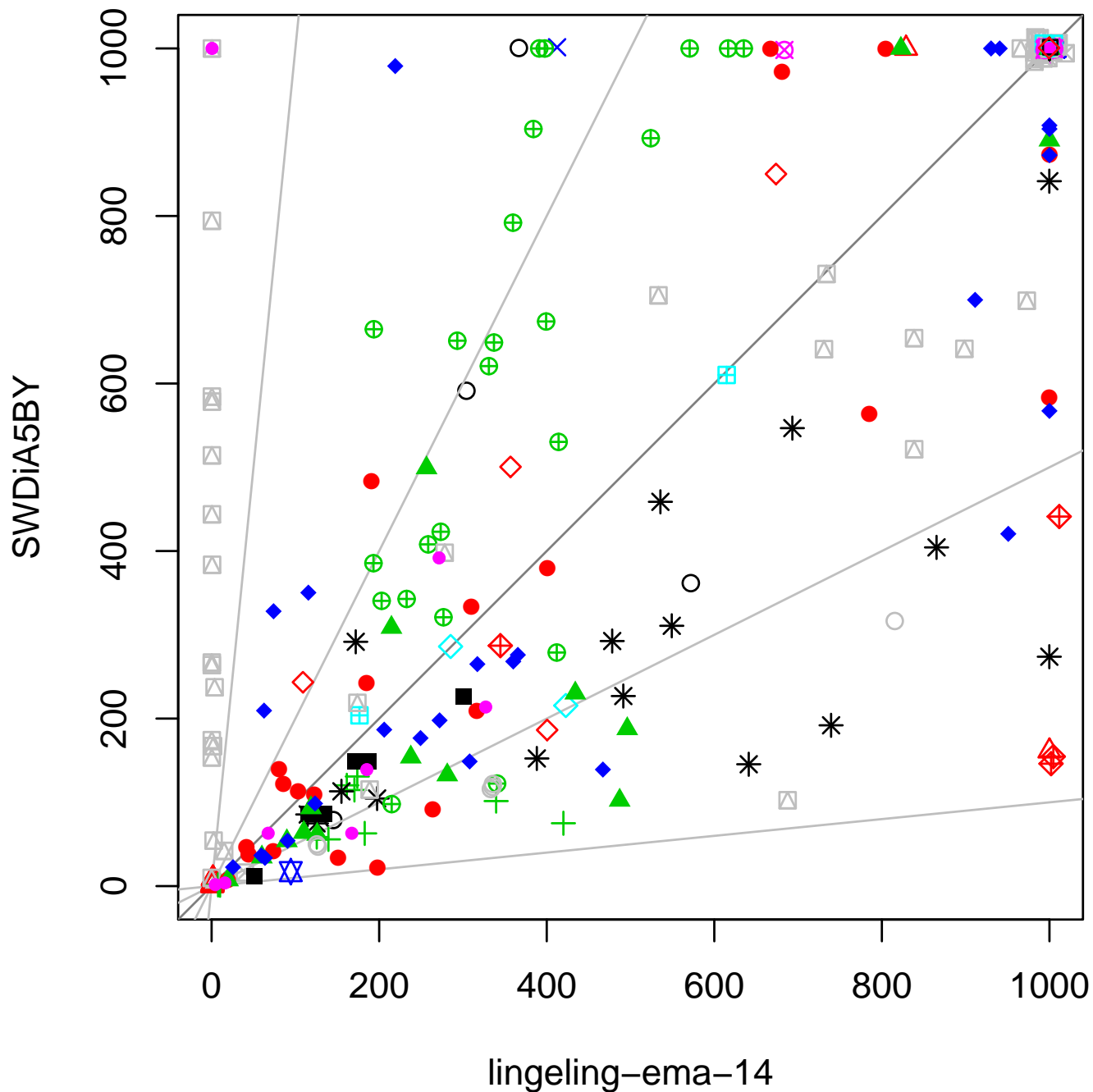
in total **300** instances clustered in **23** buckets

lingeling-sc2014 versus SWDiA5BY



- 2d-strip-packing
- △ argumentation
- + bio
- × crypto-aes
- ◇ crypto-des
- ▽ crypto-gos
- ⊠ crypto-md5
- * crypto-sha
- ◇ crypto-vpmc
- ⊕ diagnosis
- ☆ fpga-routing
- ⊞ hardware-bmc
- ⊠ hardware-bmc-ibm
- ⊠ hardware-cec
- hardware-manolios
- hardware-velev
- ▲ planning
- ◆ scheduling
- scheduling-pesp
- software-bit-verif
- software-bmc
- symbolic-simulation
- ◇ termination

lingeling-ema-14 versus SWDiA5BY



- 2d-strip-packing
- △ argumentation
- + bio
- × crypto-aes
- ◇ crypto-des
- ▽ crypto-gos
- ⊠ crypto-md5
- * crypto-sha
- ◇ crypto-vpmc
- ⊕ diagnosis
- ⊠ fpga-routing
| ⊠ hardware-bmc | ⊠ hardware-bmc-ibm |
| ⊠ hardware-cec | ■ hardware-manolios |
| ● hardware-velev | ▲ planning |
| ◆ scheduling | ⊠ scheduling-pesp |
| ● software-bit-verif | ○ software-bmc |
| □ symbolic-simulation | ◇ termination |

```
Status run_CDCL_loop_with_restarts () {
    for (;;) {
        if (bcp ()) {
            if (restarting ()) restart ();
            else if (!decide ()) return SATISFIABLE;
        } else {
            conflicts++;
            if (!analyze ()) return UNSATISFIABLE;
        }
    }
}
```

- run BCP and conflict analysis (including learning) until completion
- restart if restart policy implemented in `restarting` says so
- usually based on a global `conflicts` counter
- otherwise pick next decision (unless all are assigned)

```
bool restarting () {  
    return conflicts >= limit;  
}
```

```
void static_uniform_restart () {  
    restarts++;  
    limit = conflicts + interval;  
    backtrack (0);  
}
```

```
void static_geometric_restart () {  
    limit = conflicts + interval * pow (1.5, ++restarts);  
    backtrack (0);  
}
```

```
void luby_restart () {  
    limit = conflicts + interval * luby (++restarts);  
    backtrack (0);  
}
```

■ static schemes

- fixed schedule of restarts only based on conflicts counter
 - **uniform intervals:** wait a fixed number of conflicts after each restart
 - **non-uniform restart intervals**
 - number of performed restarts determines next interval (in terms of conflicts)
 - arithmetically or geometrically increasing actual interval
 - Luby scheme (also known as reluctant doubling)
 - inner-outer scheme

■ dynamic schemes

- agility based restart blocking
- local restarts (not discussed in the paper nor the talk)
- reusing the trail implicitly also blocks restarts (even partially)
- Glucose restart scheme (focus here)

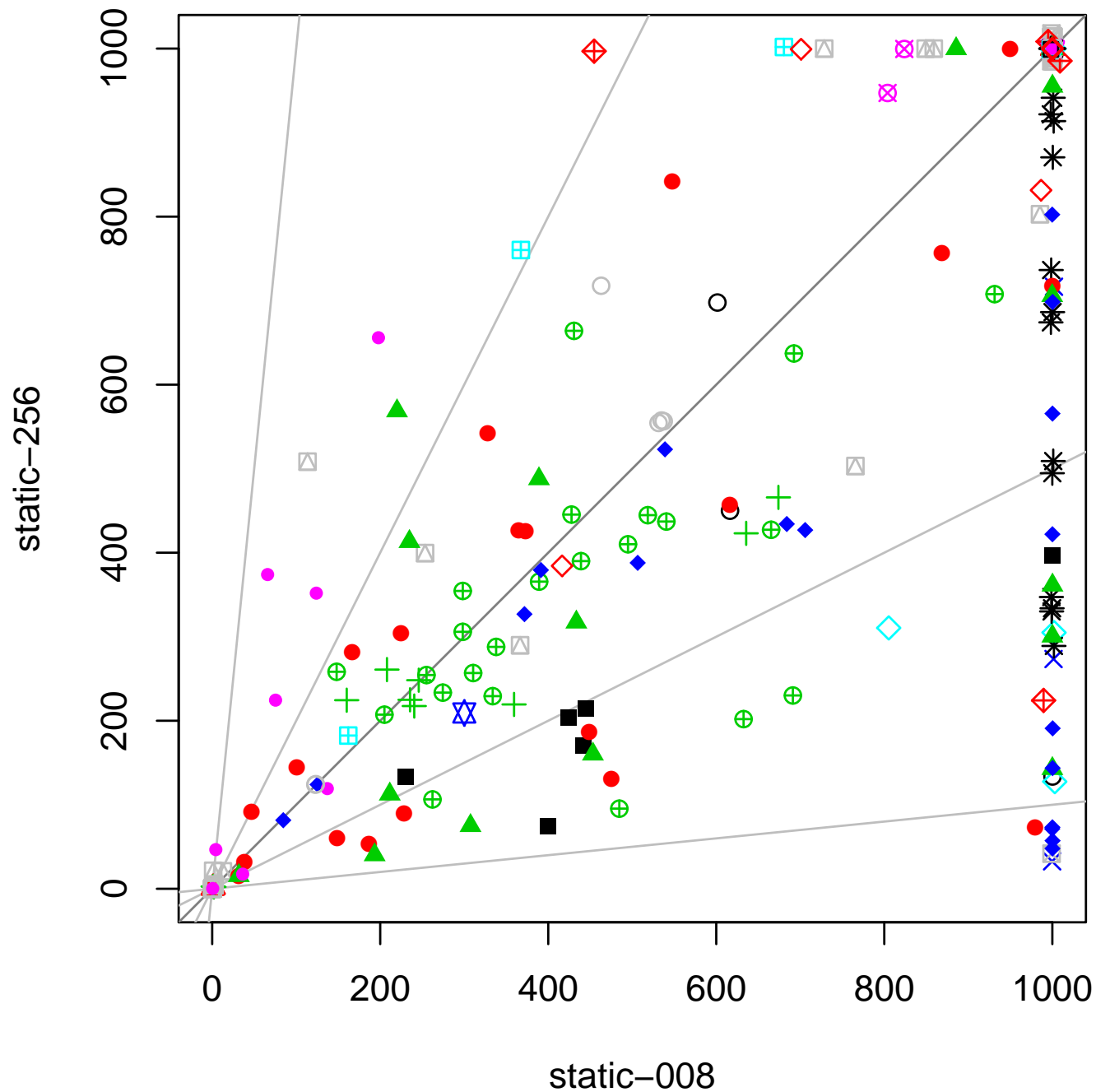
r	002	004	008	016	032	064	128	256	512
tot	122	127	139	144	144	161	163	168	158
sat	40	41	49	56	56	73	76	83	79
uns	82	86	90	88	88	88	87	85	79

r	002	004	008	016	032	064	128	256	512
2d-strip-packing	0/ <u>2</u>	0/ <u>2</u>	0/ <u>2</u>	0/ <u>2</u>	0/ <u>2</u>	1/ <u>2</u>	1/ <u>2</u>	1/ <u>2</u>	2/<u>2</u>
crypto-sha	0/0	0/0	0/0	0/0	1/0	7/0	11/0	13/0	10/0
hardware-cec	0/22	0/23	0/<u>24</u>	0/22	0/22	0/23	0/22	0/21	0/21
hardware-manolios	0/4	0/5	0/5	0/5	0/<u>6</u>	0/<u>6</u>	0/<u>6</u>	0/<u>6</u>	0/<u>6</u>
hardware-velev	5/9	6/10	<u>8/11</u>	<u>8/12</u>	<u>8/12</u>	8/<u>13</u>	<u>8/12</u>	<u>8/11</u>	<u>8/6</u>
planning	6/3	6/ <u>5</u>	7/4	7/4	8/4	9/3	9/4	11/4	10/4
scheduling	1/ <u>7</u>	0/ <u>7</u>	1/ <u>7</u>	4/ <u>7</u>	6/ <u>7</u>	9/ <u>7</u>	9/ <u>7</u>	11/ <u>7</u>	12/<u>7</u>

SAT / UNSAT

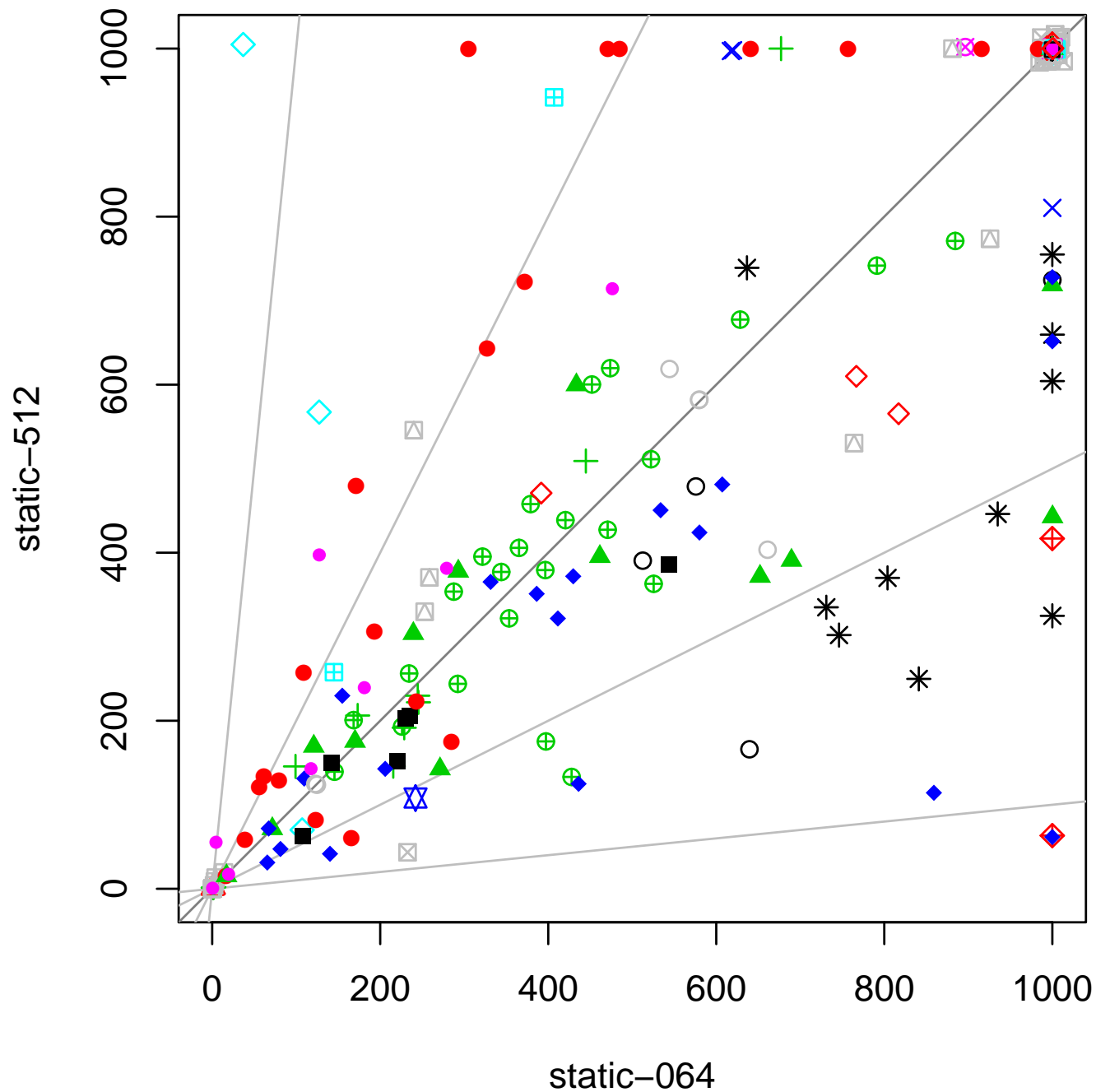
underlined best

static-008 versus static-256



- 2d-strip-packing
- △ argumentation
- + bio
- × crypto-aes
- ◇ crypto-des
- ▽ crypto-gos
- ⊠ crypto-md5
- * crypto-sha
- ◇ crypto-vpmc
- ⊕ diagnosis
- ☆ fpga-routing
- ⊞ hardware-bmc
- ⊗ hardware-bmc-ibm
- ⊠ hardware-cec
- hardware-manolios
- hardware-velev
- ▲ planning
- ◆ scheduling
- scheduling-pesp
- software-bit-verif
- software-bmc
- symbolic-simulation
- ◇ termination

static-064 versus static-512



- 2d-strip-packing
- △ argumentation
- + bio
- × crypto-aes
- ◇ crypto-des
- ▽ crypto-gos
- ⊠ crypto-md5
- * crypto-sha
- ◇ crypto-vpmc
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```
bool restarting () {
    return conflicts >= limit &&
        average_RECENT_lbds () > 1.25 * average_ALL_lbds ();
}

void glucose_restart () { // same as static_uniform_restart
    restarts++;
    limit = conflicts + 50;
    backtrack (0);
}
```

- glucose level (LBD) of learned clause:
 - number of different decision levels in a learned clauses
 - calculated at the point the clause is learned during conflict analysis
- last 50 LBDs are stored and considered recent (explicit LBD queue)
- total average of all LBDs is simply $\text{sum_lbd} / \text{conflicts}$
- for discussion of blocking restarts since Glucose 2.1 see the paper

Glucose uses *simple moving average* (*SMA*) for the average of recent LBDs and *cumulative moving average* (*CMA*) for the the average of all LBDs and

simple $SMA(n, w) = \frac{1}{w} \cdot (t_n + t_{n-1} + \dots + t_{n-w+1})$ with $n \geq w \geq 1$

cumulative $CMA(n) = SMA(n, n)$

$$CMA(n) = CMA(n-1) + \frac{t_n - CMA(n-1)}{n}$$

$$SMA(n, w) = SMA(n-1, w) + \frac{t_n}{w} - \frac{t_{n-w}}{w}$$

requires $SMA(n, 50) > 1.25 \cdot CMA(n)$ to restart
and 50 conflicts have passed

we suggest to use *EMAs* instead of the “fast” *SMA* and/or “slow” *CMA*

exponential $EMA(n, \alpha) = \alpha \cdot t_n + (1 - \alpha) \cdot EMA(n - 1, \alpha)$ with $0 < \alpha < 1$ $a \approx \frac{2}{1+w}$

alternative $EMA(n, \alpha) = EMA(n - 1, \alpha) + \alpha \cdot (t_n - EMA(n - 1, \alpha))$

current estimate

next estimate

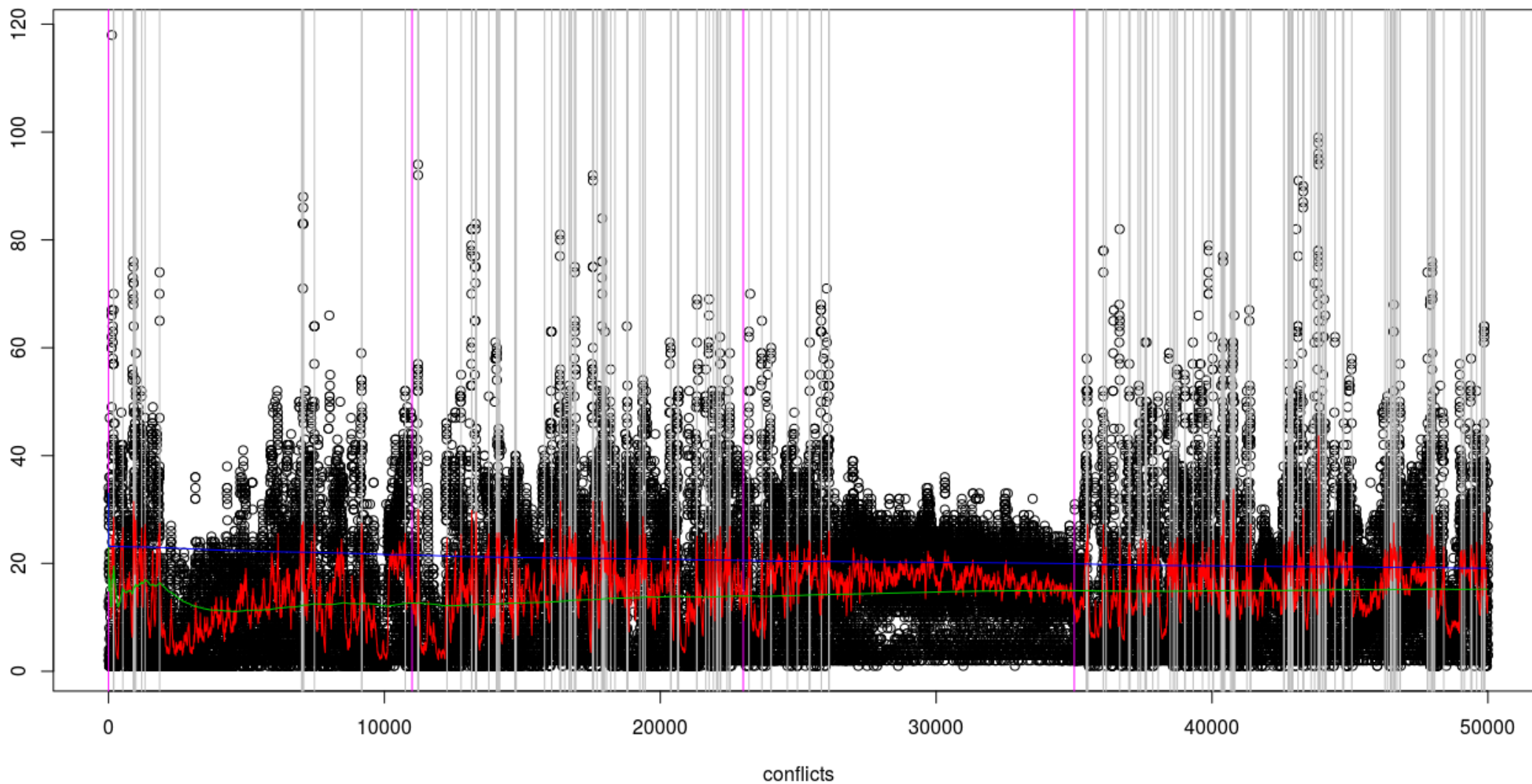
difference/error

to restart version average requires $EMA(n, 2^{-5}) > 1.25 \cdot CMA(n)$

to restart version ema-14 requires $EMA(n, 2^{-5}) > 1.25 \cdot EMA(n, 2^{-14})$

and again in both cases that a certain number of conflicts say 50 have passed

- LBD
- | restart
- | inprocessing
- fast *EMA* of LBD with $\alpha = 2^{-5}$
- slow *EMA* of LBD with $\alpha = 2^{-14}$ (ema-14)
- *CMA* of LBD (average)



solver	Glucose 4.0			Lingeling ba2							
	<i>ss</i>	<i>es</i>	<i>ee</i>	<i>avg</i>	<i>e8</i>	<i>e10</i>	<i>e12</i>	<i>e14</i>	<i>e16</i>	<i>e18</i>	<i>e20</i>
tot	163	163	165	178	167	170	180	181	180	177	171
sat	72	73	76	83	80	78	86	86	86	82	77
uns	91	90	89	95	87	92	94	95	94	95	94
avgc	192	166	167	145	230	204	195	186	172	147	108

Glucose 4.0 column *ss* correspond to the original Glucose version

column *es* to adding EMAs for only forcing restarts

column *ee* includes using EMA for blocking restarts too

column *avg* is Lingeling version *average* of Glucose version *ee*

columns *eX* correspond to Lingeling versions *ema-X*

using a slow EMA with $\alpha = 2^{-X}$ instead of CMA


```
double fast, slow;
```

```
...
```

```
bool analyze () {
```

```
    int lbd;
```

```
    ...
```

```
    slow += (lbd - slow) / (double) (1<<14);
```

```
    fast += (lbd - fast) / (double) (1<<5);
```

```
    ...
```

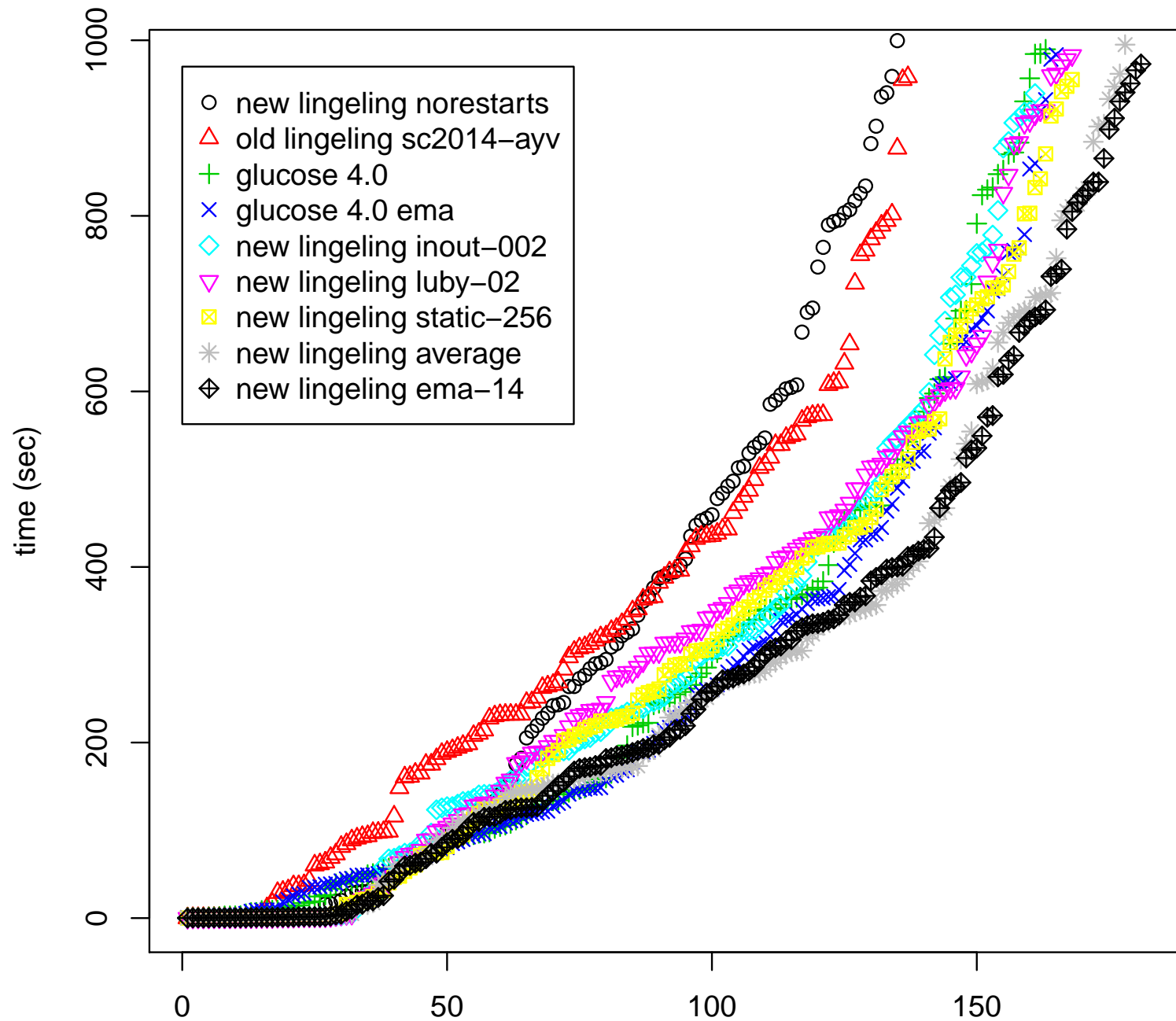
```
}
```

```
bool restarting () {
```

```
    return conflicts > limit && fast > 1.25 * slow;
```

```
}
```

- data and source: <http://fmv.jku.at/evalrestart/evalrestart.7z>
- optimal restart interval varies with benchmark bucket
 - for miters fast restarts essential
 - for crypto benchmarks longer intervals necessary
 - disabling restarts completely is bad
 - Glucose restarts superior to Luby style
- presented an EMA variant of the Glucose restart scheme
 - simpler model, simpler to implement
 - similar performance (slightly faster)
- future work
 - how to improve blocking of restarts
 - restart intervals still not optimal: really need machine learning?
 - combined SAT and Stock Market Analysis or SAT and Reinforcement Learning
originally proposed title for the POS'15 paper



solved SAT competition 2014 application instances (ordered by solving time)

- decision heuristics
 - empirical insight: VMTF simpler and as good as VSIDS
 - simpler theoretical model
 - can we prove why these work?
- restarts
 - another look at restarts in SAT
 - simplified Glucose restart scheme, showed that it somehow works
 - clearly not where we want it to be (machine learning necessary?)