

Boolector 1.2

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- SMT solver
- Theories
 - QF_BV
 - QF_AUFBV
- Model Checker for safety properties
 - Sequential and synchronous circuits with registers and memories
 - BTOR

- Optimization techniques
 - Bounded rewriting
 - Global term substitution techniques
 - Arithmetic normalization on demand
 - Local two level rewriting
 - Linear equation solver
 - Domain abstraction
 - ...

- And-Inverter Graph Synthesis
 - Each expression is synthesized into AIG
 - Local two level AIG rewriting [BrummayerBiere06]
 - AIG is encoded into CNF
 - SAT solvers
 - * Precosat is used for QF_BV
 - * Picosat is used for QF_AUFBV
- Lemmas on demand for QF_AUFBV [BrummayerBiere09]
 - Decision procedure for extensional theory of arrays