





The Department of Computer Science of Johannes Kepler University Linz¹ together with the Austrian Society of Computer Science (ÖGI) invites to the following talk:

Sebastian Gabmeyer Technical University of Darmstadt

Symbolic Verification of Graph Transformation Systems with Hardware Model Checkers

March 1st, 2017, 13:00 – 14:00 Johannes Kepler Universitity Linz, Science Park 3 HS 19

Abstract: In this talk I present a novel symbolic bounded model checking approach to test reachability properties of model-driven software implementations. Given a concrete initial state of a software system, a type graph, and a set of graph transformations, which describe the system's structure and its behavior, the system is verified against a reachability property. This reachability property is expressed in terms of a graph constraint. Without any user intervention, the presented approach exploits state-of-the-art model checking technologies successfully used in hardware industry and reports the result back to the user. I demonstrate the efficiency of the approach based on a case study.

About the Speaker: Sebastian Gabmeyer is a post-doc at TU Darmstadt since January 2016. He finished his PhD on model checking based verification techniques for graph transformations under the supervision of Gerti Kappel and Martina Seidl in 2015. After a brief intermezzo in industry he joined the Security Engineering Group led by Stefan Katzenbeisser where he works on hardware based cryptography like Physical Unclonable Functions and remote software attestation.

Host: Assoz.-Profⁱⁿ. Drⁱⁿ. Martina Seidl

¹The department consists of the following institutes:

Anwendungsorientierte Wissensverarbeitung (FAW), Bioinformatik, Computational Perception, Computer-Architektur, Computergrafik, Formale Modelle und Verifikation, Informationsverarbeitung und Mikroprozessortechnik (FIM), Integrierte Schaltungen, Pervasive Computing, Systems Engineering and Automation, Systemsoftware, Telekooperation

