

FMCAD 2009 EDA Vendors Panel

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Topics

- Our customer's most pressing problems
- Translation of problems into research agenda
- Uninspiring research topics

Customer Problems

- Functional Verification Cost
- Who is the customer?
- Performance/Capacity – big surprise
- Constraints. Writing and debugging them.
- Verification closure. When am I done.
- Ease of use. This is really a form of cost.

A Research Agenda

- Proof/Falsification performance and capacity
- “TestBench Style” (svtb) constraints in MC
- Meaningful assertion completeness
- Property behavior visualization and debug

Uninspiring FV Research

- It is not about you
- Non-automatic things
- Things that don't scale to their problem size
- Things that dictate a flow
- Weird, esoteric things they need to know